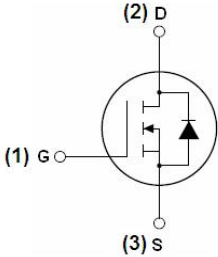

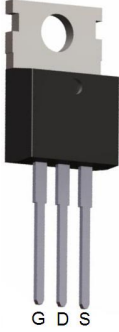


## QIAOXIN N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The VCRR8295A uses advanced trench technology and design to provide excellent <math>R_{DS(ON)}</math> with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS} = 82V, I_D = 95A</math></li> <li>● <math>R_{DS(ON)} &lt; 7.0 m\Omega @ V_{GS} = 10V</math> (Typ: 6m<math>\Omega</math>)</li> <li>● High density cell design for ultra low Rdson</li> <li>● Fully characterized avalanche voltage and current</li> <li>● Special designed for convertors and power controls</li> <li>● Good stability and uniformity with high <math>E_{AS}</math></li> <li>● Excellent package for good heat dissipation</li> <li>● Special process technology for high ESD capability</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switching application</li> <li>● Hard switched and High frequency circuits</li> <li>● Uninterruptible power supply</li> </ul>	<div style="text-align: center;">  <p><b>Schematic diagram</b></p>  <p><b>Marking and pin assignment</b></p>  <p><b>TO-220-3L top view</b></p> </div>
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### Package Marking and Ordering Information

Device Marking	Device	Device Package
VCRR8295A		TO-220-3L

### Absolute Maximum Ratings ( $T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	82	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	95	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_D(100^\circ C)$	67	A
Pulsed Drain Current	$I_{DM}$	320	A
Maximum Power Dissipation	$P_D$	170	W
Derating factor		1.13	W/ $^\circ C$
Single pulse avalanche energy <sup>(Note 5)</sup>	$E_{AS}$	529	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

## Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	0.88	$^{\circ}\text{C}/\text{W}$
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## Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

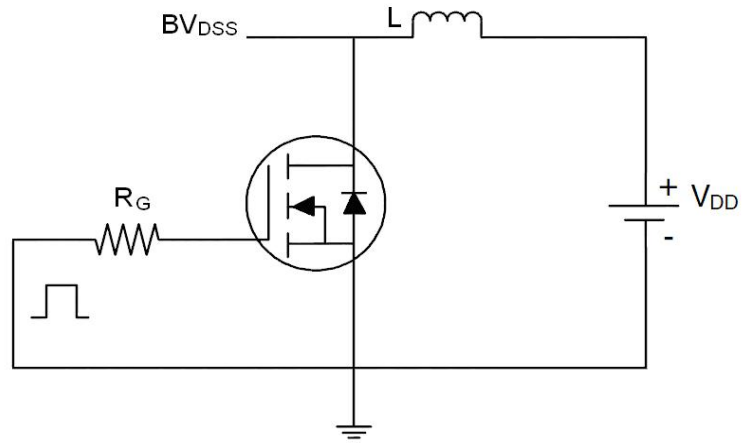
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	82	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=82V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$	-	6	7.0	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=20A$	-	50	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{iss}$	$V_{DS}=40V, V_{GS}=0V,$ $F=1.0\text{MHz}$	-	5633	-	PF
Output Capacitance	$C_{oss}$		-	268	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	226	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=40V, R_L=15\Omega$ $R_G=2.5\Omega, V_{GS}=10V$	-	18	-	nS
Turn-on Rise Time	$t_r$		-	12	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	56	-	nS
Turn-Off Fall Time	$t_f$		-	15	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=40V, I_D=50A,$ $V_{GS}=10V$	-	109.3	-	nC
Gate-Source Charge	$Q_{gs}$		-	35.1	-	nC
Gate-Drain Charge	$Q_{gd}$		-	25.8	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{SD}$	$V_{GS}=0V, I_S=95A$	-	-	1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_S$		-	-	95	A
Reverse Recovery Time	$t_{rr}$	$T_J=25^{\circ}\text{C}, I_F=100A$ $di/dt=100A/\mu s$ <sup>(Note 3)</sup>	-	-	37	nS
Reverse Recovery Charge	$Q_{rr}$		-	-	58	nC

### Notes:

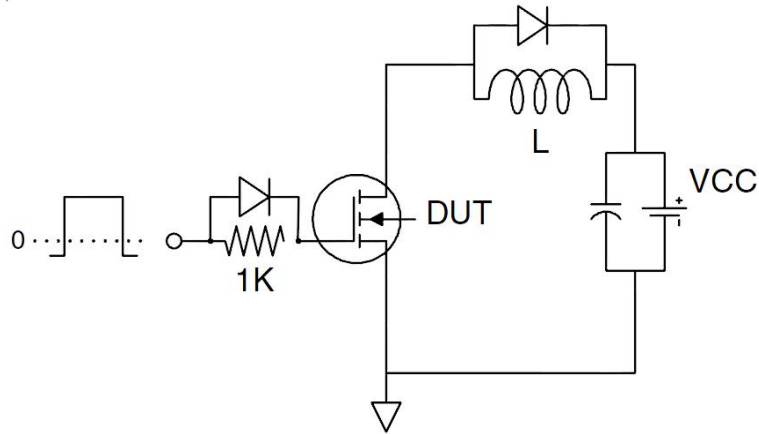
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition:  $T_J=25^{\circ}\text{C}, V_{DD}=40V, V_G=10V, L=0.5\text{mH}, R_G=25\Omega$

## Test Circuit

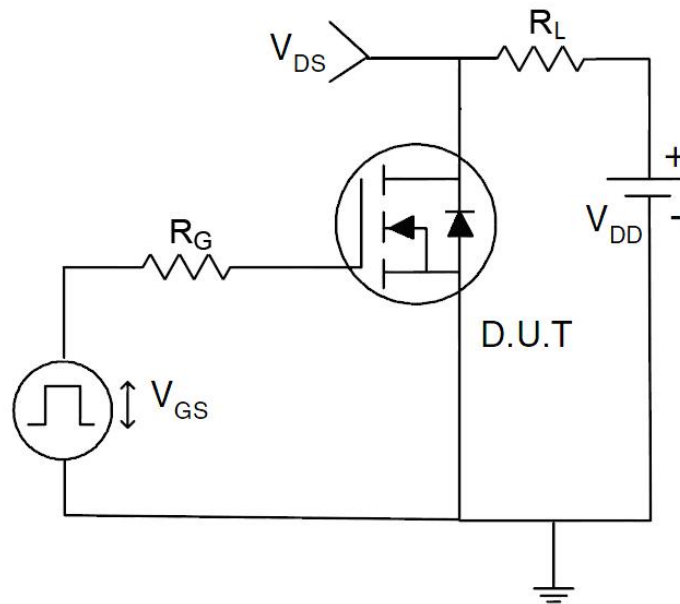
### 1) EAS Test Circuits



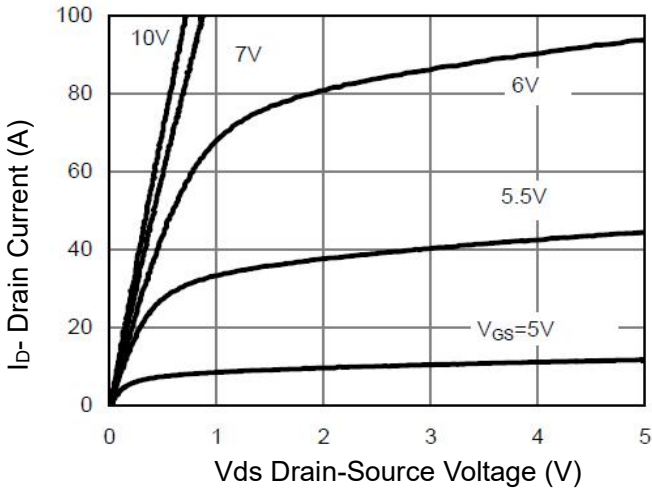
### 2) Gate Charge Test Circuit



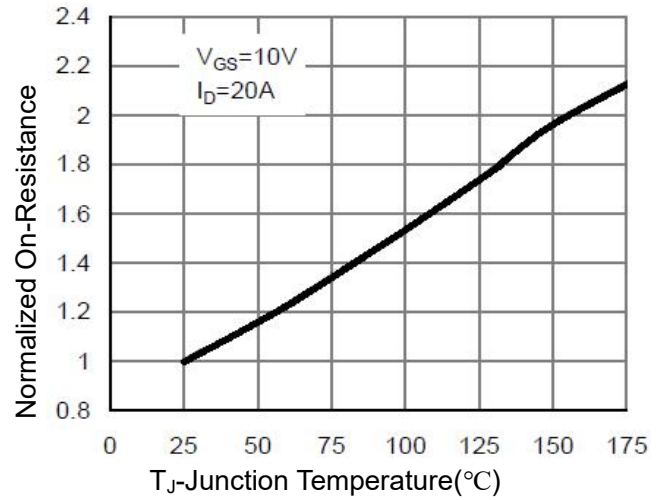
### 3) Switch Time Test Circuit



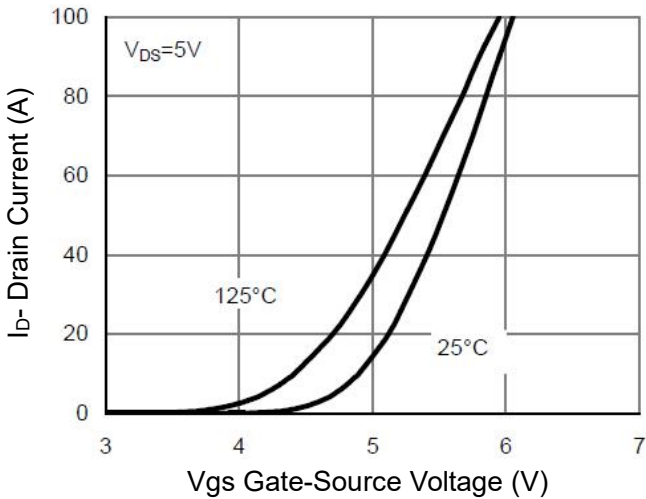
**Typical Electrical and Thermal Characteristics (Curves)**



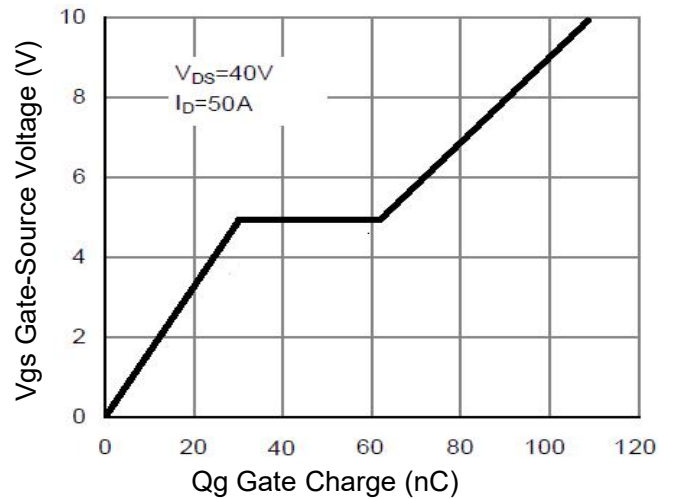
**Figure 1 Output Characteristics**



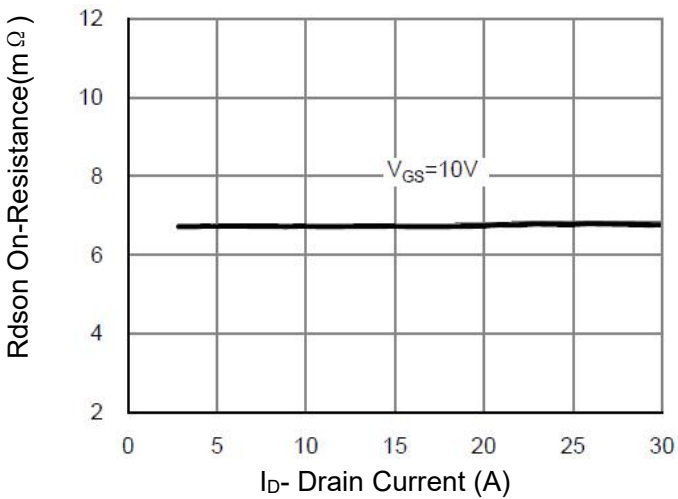
**Figure 4  $R_{ds(on)}$ -Junction Temperature**



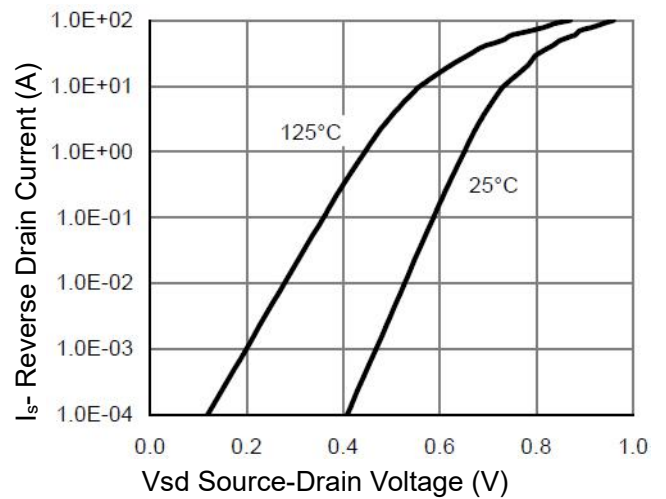
**Figure 2 Transfer Characteristics**



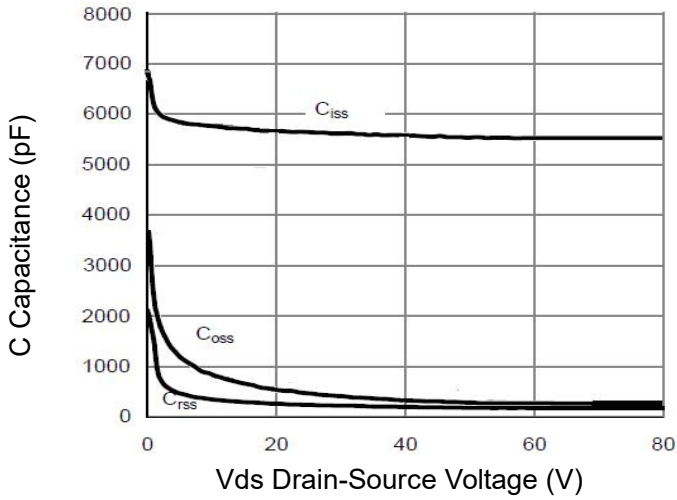
**Figure 5 Gate Charge**



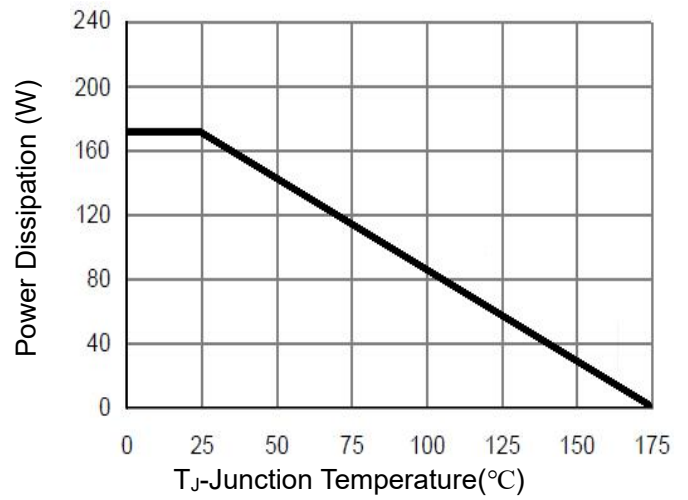
**Figure 3  $R_{ds(on)}$ - Drain Current**



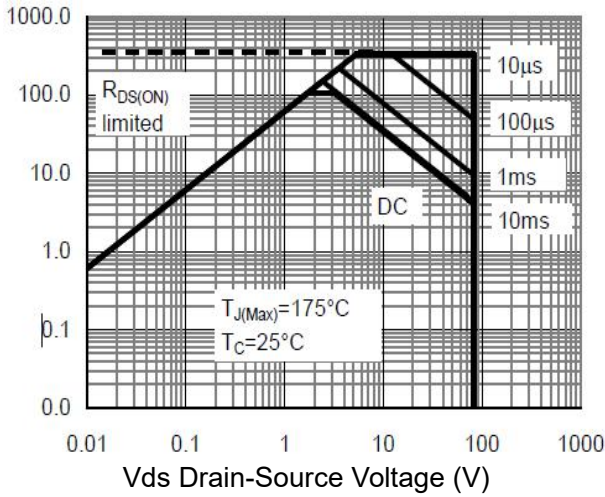
**Figure 6 Source- Drain Diode Forward**



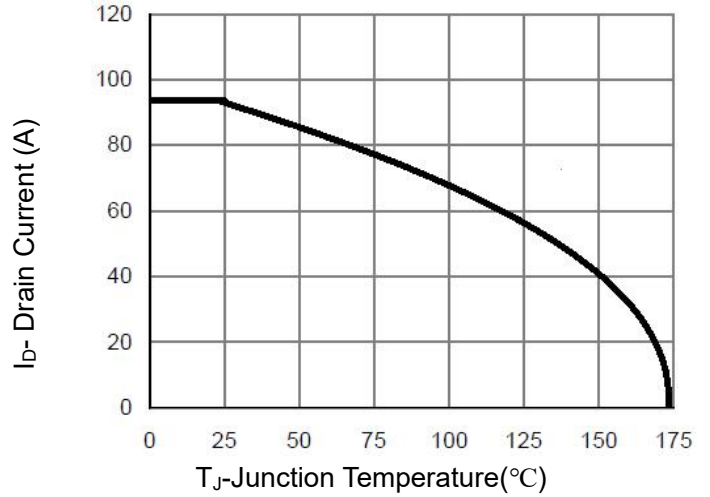
**Figure 7 Capacitance vs Vds**



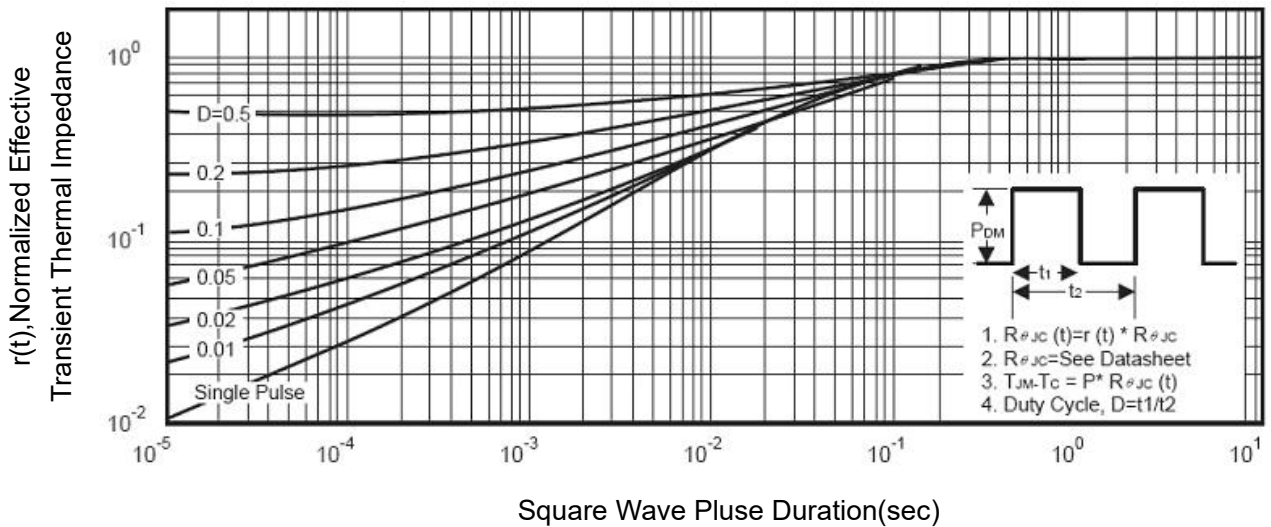
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area**

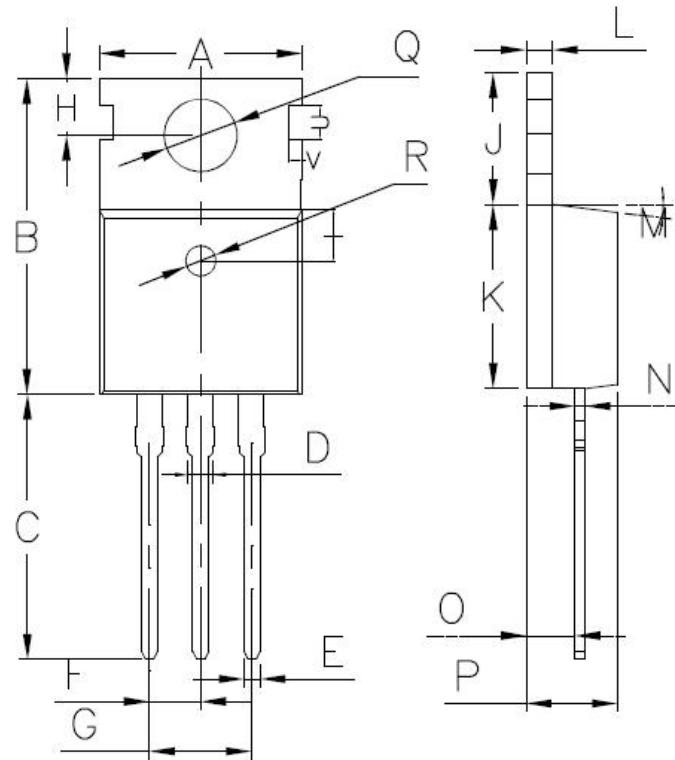


**Figure 10 Id Current De-rating**



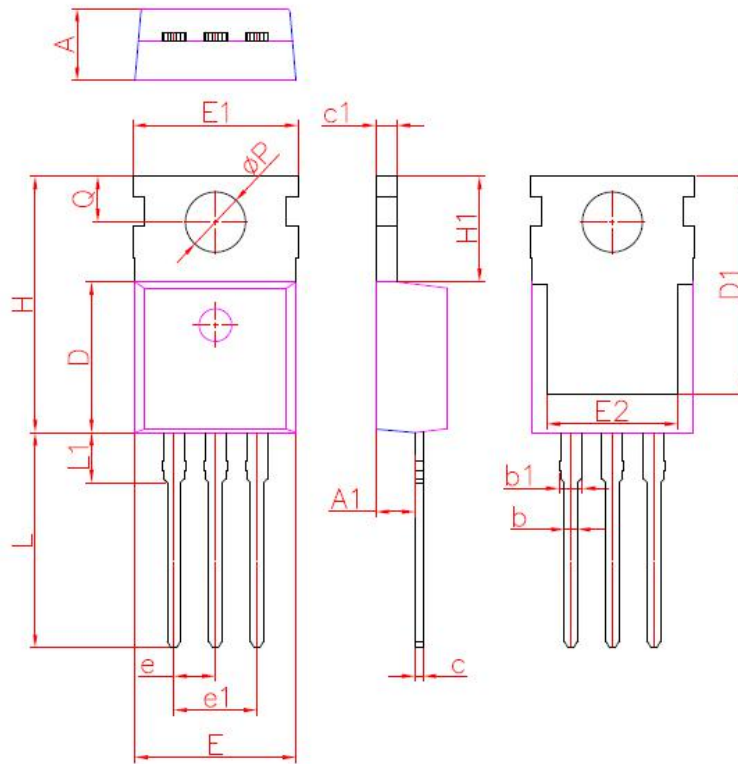
**Figure 11 Normalized Maximum Transient Thermal Impedance**

**TO-220(S) Package Information**



Symbol	Min	Non	Max
A	9.80	10.00	10.20
B	15.40	15.60	15.80
C	13.02	13.37	13.72
D	1.18	1.31	1.44
E	0.70	0.80	0.90
F	2.42	2.54	2.66
G	4.84	5.08	5.32
H	2.73	2.80	2.87
I	2.40	2.50	2.60
J	6.40	6.50	6.60
K	9.00	9.10	9.20
L	1.29	1.30	1.32
M	6.5°	7.0°	7.5°
N	0.48	0.50	0.56
O	2.35	2.4	2.5
P	4.4	4.5	4.7
Q	3.5	3.6	3.7
R	1.3	1.4	1.5
S	2°	2.5°	3°
U	1.65	1.75	1.85
V	0.58	0.68	0.78

## TO-220(E) Package Information



TO220			
DIM.	MIN.	NOM.	MAX.
A	4.20	4.40	4.60
A1	2.25	2.40	2.55
b	0.70	0.80	0.90
b1	1.17	1.27	1.37
c	0.33	0.50	0.65
c1	1.20	1.30	1.40
D	8.95	9.20	9.75
D1	13.10	13.30	13.50
E	9.74	9.84	10.04
E1	9.91	10.08	10.25
E2	7.90	8.00	8.10
e	2.54BSC		
e1	5.08BSC		
H	15.45	15.65	15.85
H1	6.30	6.45	6.60
L	12.90	13.13	13.40
L1	2.85	3.05	3.25
Q	2.65	2.80	2.95
$\phi P$	3.40	3.68	3.80
All dimensions in millimeters			



### **Attention**

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