

NCE N-Channel Enhancement Mode Power MOSFET

Description

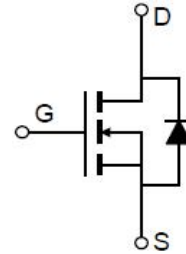
The VCRR6080AT uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

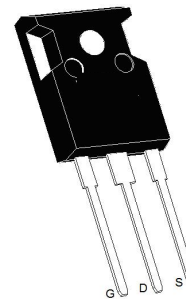
- $V_{DS} = 60V, I_D = 80A$
 $R_{DS(ON)} = 5.5m\Omega$ (typical) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 6.5m\Omega$ (typical) @ $V_{GS} = 4.5V$
- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- PWM
- Load Switching



Schematic diagram



TO-247 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VCRR6080AT		TO-247	-	-	-

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	80	A
Drain Current-Continuous($T_C = 100^\circ C$)	$I_D(100^\circ C)$	56.5	A
Pulsed Drain Current	I_{DM}	320	A
Maximum Power Dissipation	P_D	110	W
Derating factor		0.73	W/ $^\circ C$
Single pulse avalanche energy (Note 5)	E_{AS}	390	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	1.36	$^\circ C/W$
---	-----------------	------	--------------

Electrical Characteristics (T_c=25°C unless otherwise noted)

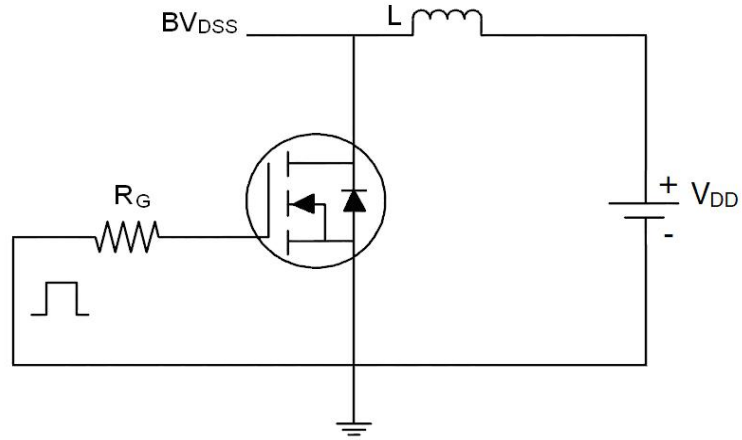
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.8	1.3	1.8	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A	-	5.5	6.3	mΩ
		V _{GS} =4.5V, I _D =20A	-	6.5	7.8	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, F=1.0MHz	-	4000	-	PF
Output Capacitance	C _{oss}		-	290	-	PF
Reverse Transfer Capacitance	C _{rss}		-	210	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =30V, R _L =1Ω V _{GS} =10V, R _G =3Ω	-	8.5	-	nS
Turn-on Rise Time	t _r		-	7	-	nS
Turn-Off Delay Time	t _{d(off)}		-	40	-	nS
Turn-Off Fall Time	t _f		-	15	-	nS
Total Gate Charge	Q _g	V _{DS} =30V, I _D =20A, V _{GS} =10V	-	90.3	-	nC
Gate-Source Charge	Q _{gs}		-	10.9	-	nC
Gate-Drain Charge	Q _{gd}		-	20.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	80	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 20A di/dt = 100A/μs (Note3)	-	32	-	nS
Reverse Recovery Charge	Q _{rr}		-	45	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

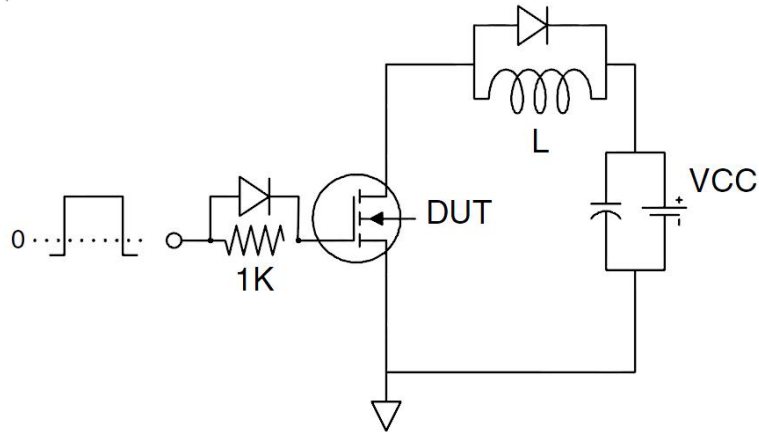
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. E_{AS} condition : T_J=25°C, V_{DD}=30V, V_G=10V, L=0.5mH, R_G=25Ω

Test circuit

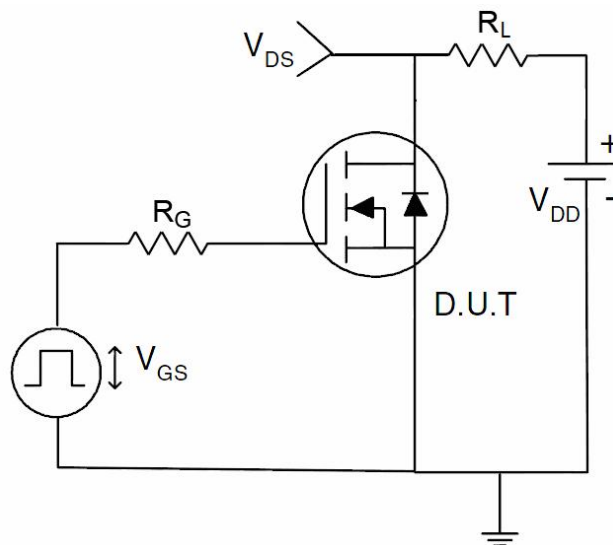
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

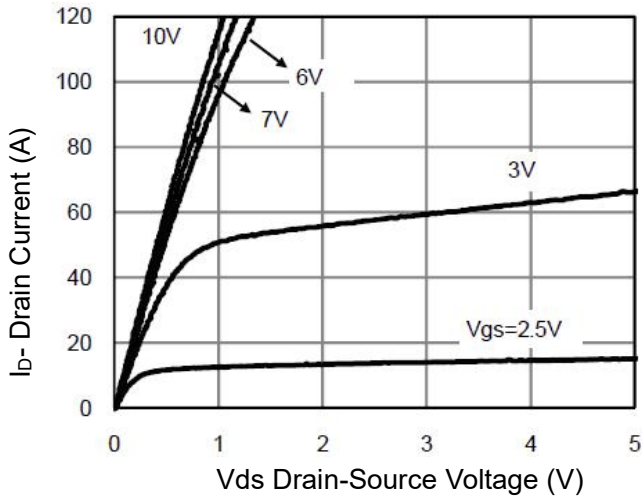


Figure 1 Output Characteristics

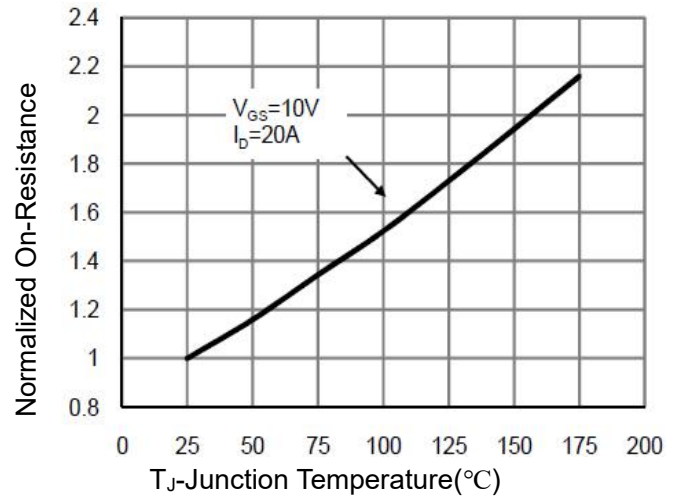


Figure 4 R_{dson} -Junction Temperature

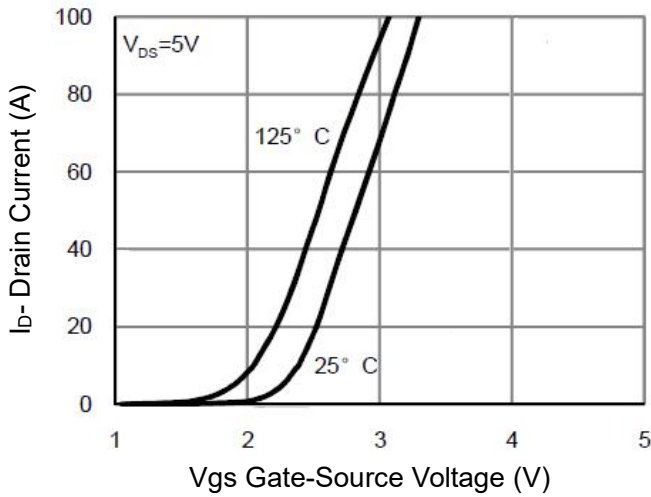


Figure 2 Transfer Characteristics

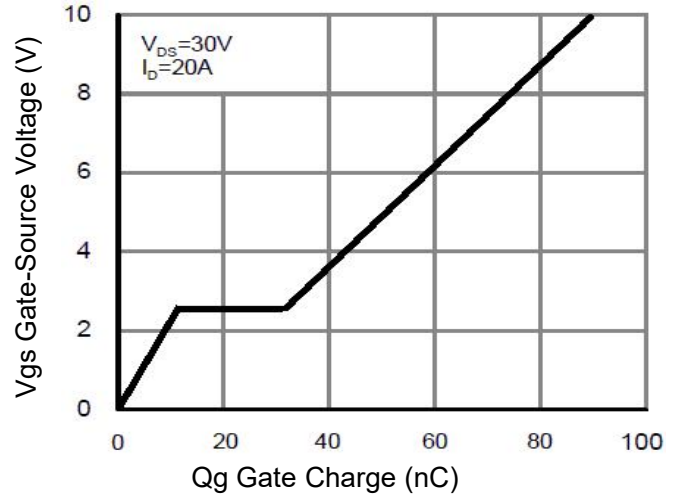


Figure 5 Gate Charge

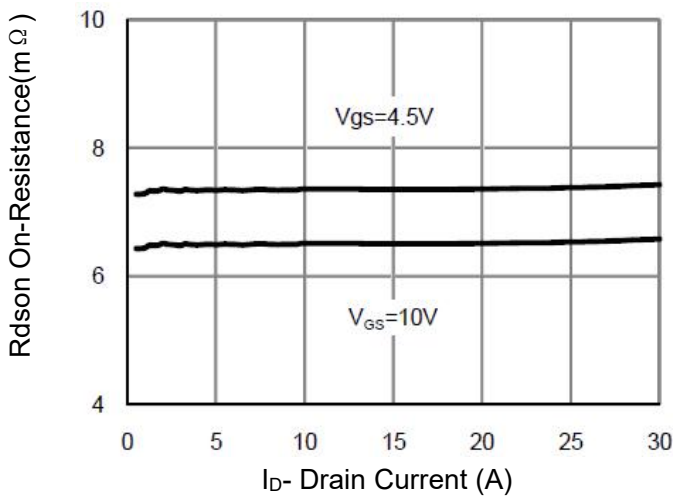


Figure 3 R_{dson} - Drain Current

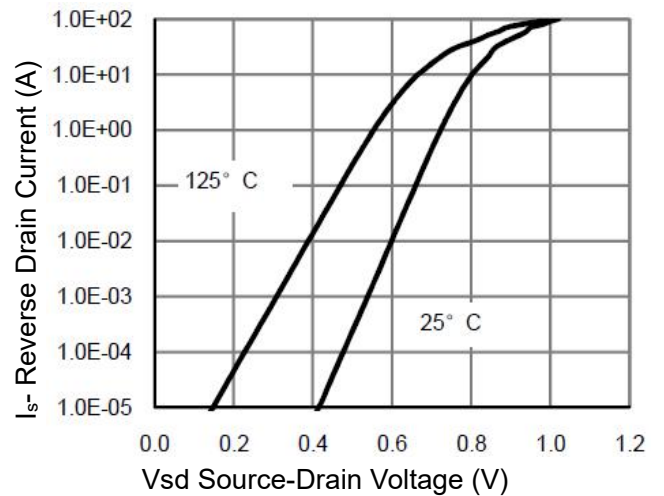


Figure 6 Source- Drain Diode Forward

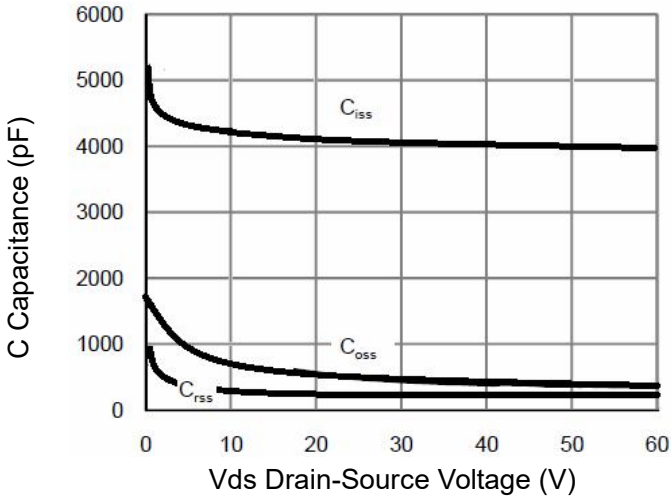


Figure 7 Capacitance vs Vds

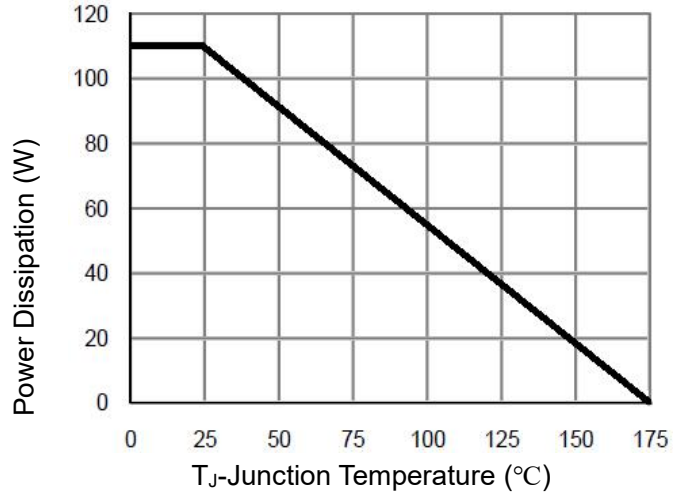


Figure 9 Power De-rating

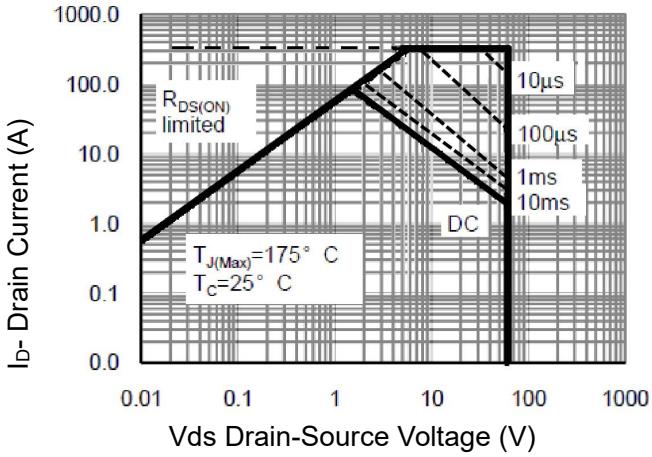


Figure 8 Safe Operation Area

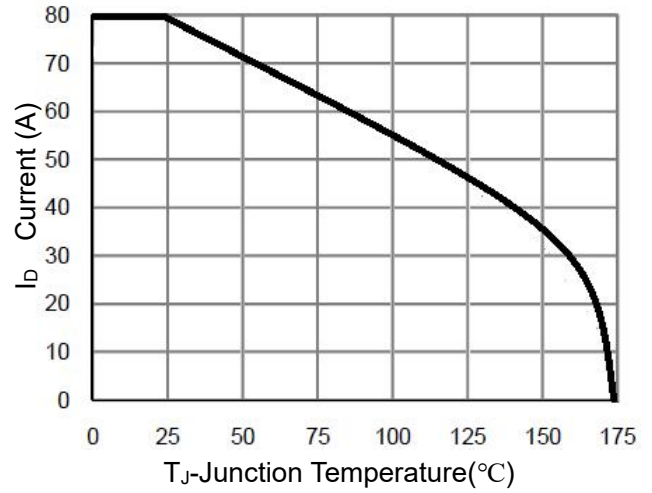


Figure 10 ID Current- Junction Temperature

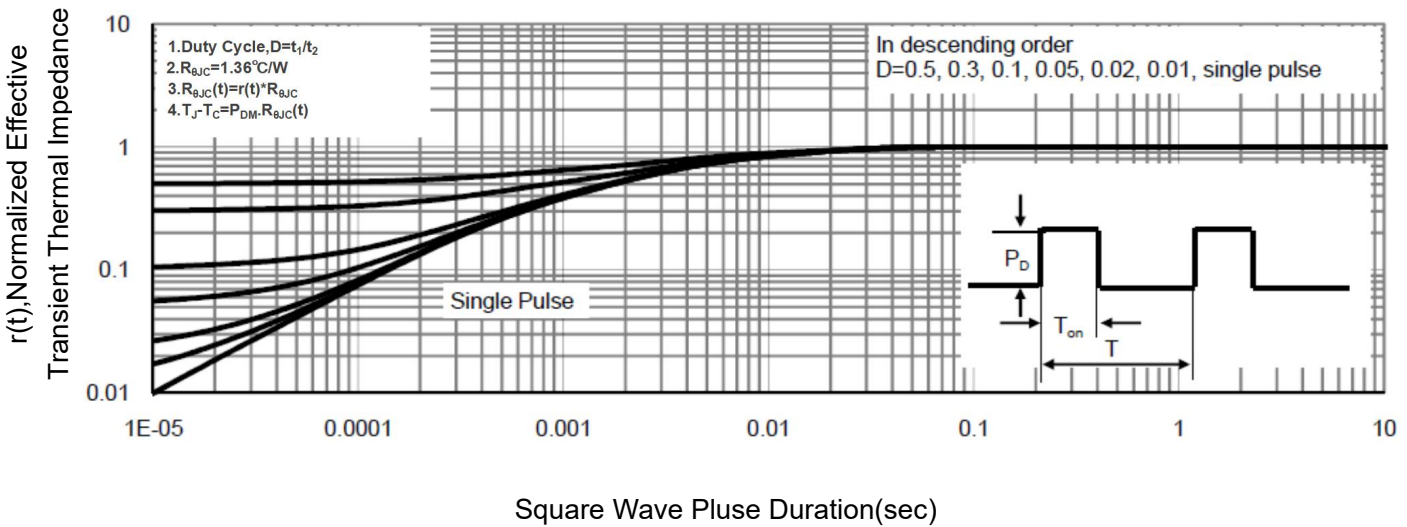
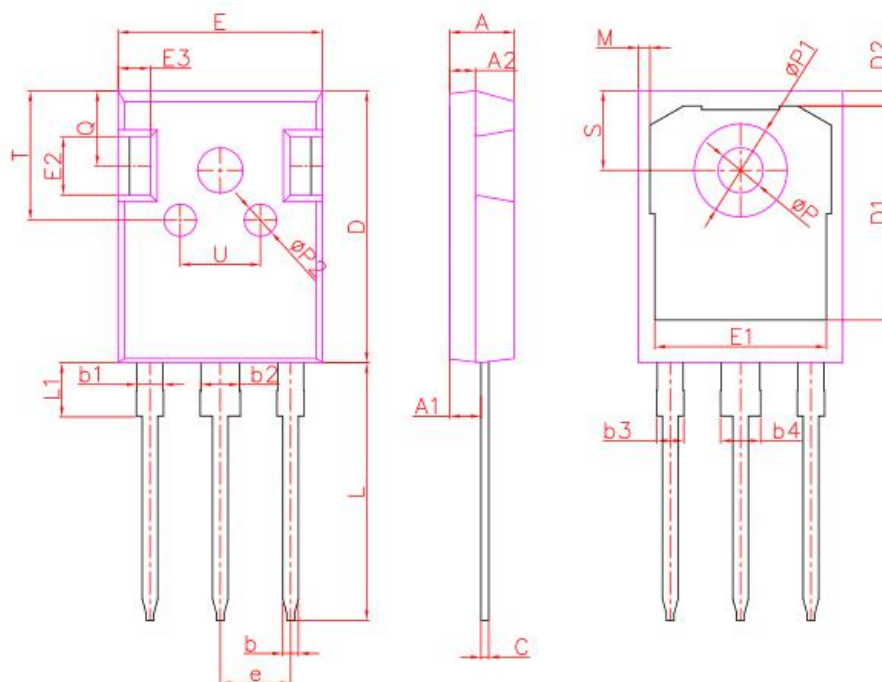


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-247 Package Information



TO247			
DIM.	MIN.	NOM.	MAX.
A	4.90	5.00	5.10
A1	2.31	2.432	2.51
A2	1.90	2.00	2.10
b	1.16	1.20	1.26
b1	1.96	2.00	2.06
b2	2.96	3.00	3.06
b3	—	—	2.25
b4	—	—	3.25
c	0.59	0.60	0.66
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.26	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
e	5.436BSC		
L	19.80	19.90	20.10
L1	—	—	4.30
M	0.35	0.89	0.95
P	3.40	3.50	3.60
P1	7.00	7.20	7.40
P2	2.40	2.50	2.60
Q	5.60	5.80	6.00
S	6.05	6.15	6.25
T	9.80	10.00	10.20
U	6.00	6.20	6.40
All dimensions in millimeters			

Attention

QIAOXIN assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all QIAOXIN products described or contained herein. QIAOXIN products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. QIAOXIN reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.