

QIAOXIN N-Channel Enhancement Mode Power MOSFET

Description

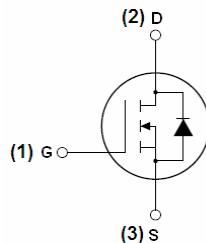
The VCRR75H35TC uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in automotive applications and a wide variety of other applications.

General Features

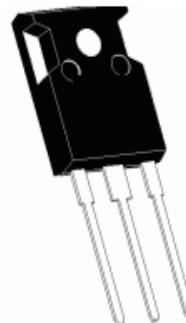
- $V_{DSS} = 75V, I_D = 350A$
- $R_{DS(ON)} < 2.2m\Omega @ V_{GS}=10V$ (Typ: 1.7 mΩ)
- Good stability and uniformity with high E_{AS}
- Special process technology for high ESD capability
- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



TO-247 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VCRR75H35TC	VCRR75H35TC	TO-247	-	-	

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DSS}	75	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	350	A
Drain Current-Continuous($T_C=100^\circ C$)	$I_D (100^\circ C)$	270	A
Pulsed Drain Current	I_{DM}	1280	A
Maximum Power Dissipation	P_D	460	W
Derating factor		3.07	W/°C
Single pulse avalanche energy (Note 3)	E_{AS}	3500	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 1)	R_{eJC}	0.33	°C/W
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Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

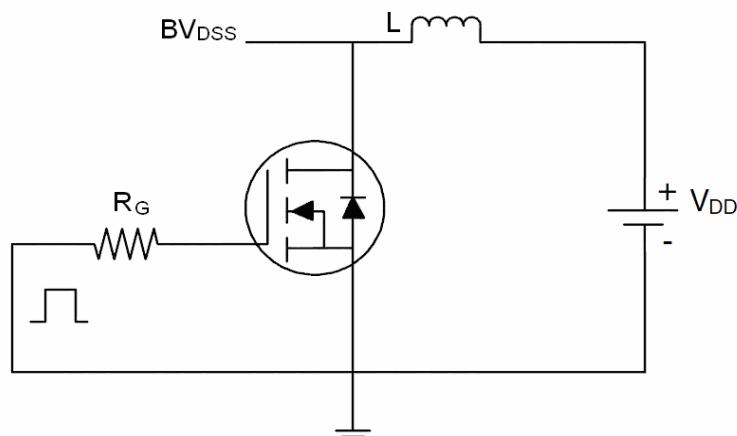
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	75	86	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=75\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±200	nA
On Characteristics						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2	3	4	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=40\text{A}$	-	1.7	2.2	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=25\text{V}, I_{\text{D}}=40\text{A}$	100	-	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	25500	-	PF
Output Capacitance	C_{oss}		-	1652	-	PF
Reverse Transfer Capacitance	C_{rss}		-	1261	-	PF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=40\text{V}, I_{\text{D}}=40\text{A}$ $V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=1.2\Omega$ (Note2)	-	50	-	nS
Turn-on Rise Time	t_r		-	235	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	180	-	nS
Turn-Off Fall Time	t_f		-	280	-	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=40\text{V}, I_{\text{D}}=40\text{A},$ $V_{\text{GS}}=10\text{V}$ ^(Note2)	-	586	-	nC
Gate-Source Charge	Q_{gs}		-	120	-	nC
Gate-Drain Charge	Q_{gd}		-	200	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=40\text{A}$	-	-	1.2	V
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, IF = 40\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ ^(Note2)	-	170	-	nS
Reverse Recovery Charge	Q_{rr}		-	500	-	nC

Notes:

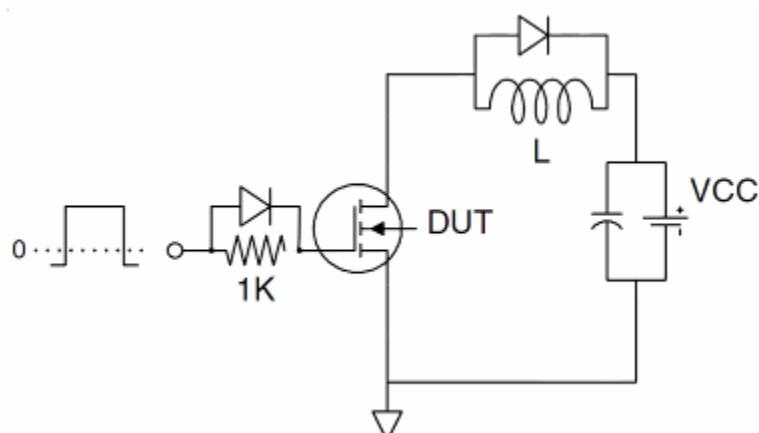
1. Surface Mounted on FR4 Board, $t \leq 10$ sec.
2. Pulse Test: Pulse Width $\leq 400\mu\text{s}$, Duty Cycle $\leq 2\%$.
3. EAS condition: $T_j=25^\circ\text{C}, V_{\text{DD}}=37.5\text{V}, V_{\text{G}}=10\text{V}, L=1\text{mH}, R_g=25\Omega$
4. $I_{\text{SD}} \leq 125\text{A}$, $di/dt \leq 260\text{A}/\mu\text{s}$, $V_{\text{DD}} \leq V_{(\text{BR})\text{DSS}}$, $T_j \leq 175^\circ\text{C}$

Test circuit

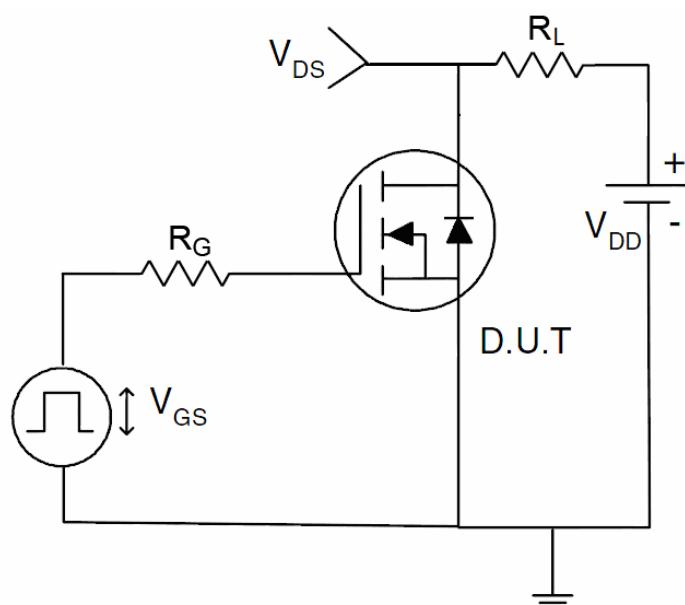
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

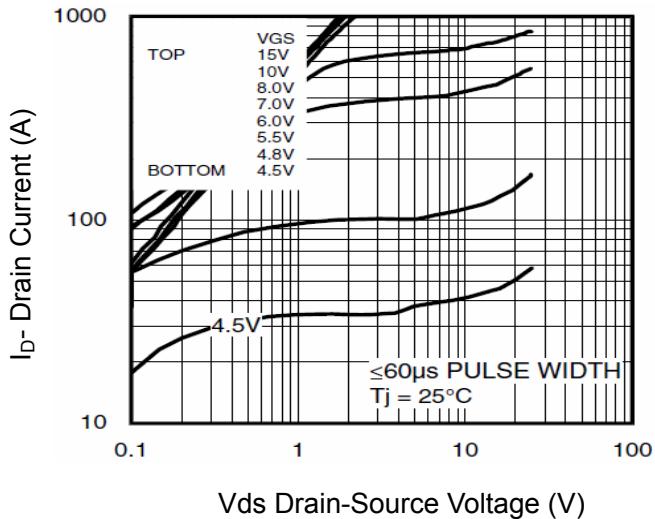


Figure 1 Output Characteristics

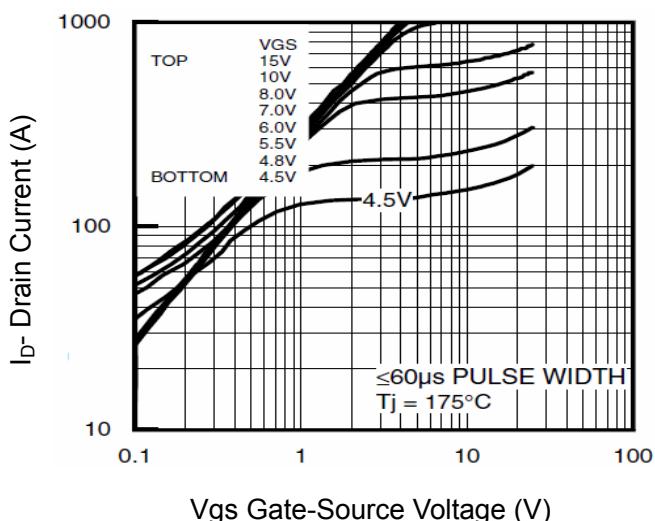


Figure 2 Transfer Characteristics

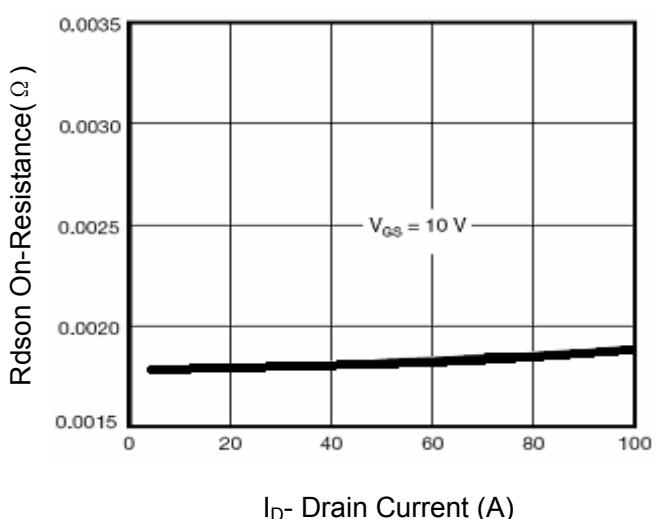


Figure 3 Rdson- Drain Current

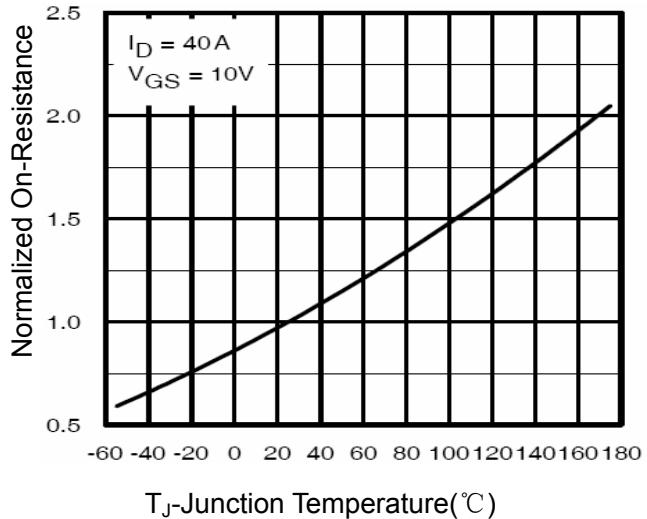


Figure 4 Rdson-JunctionTemperature

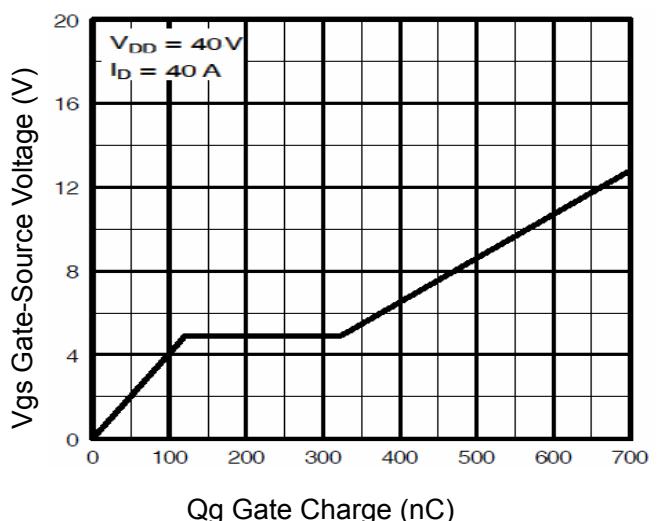


Figure 5 Gate Charge

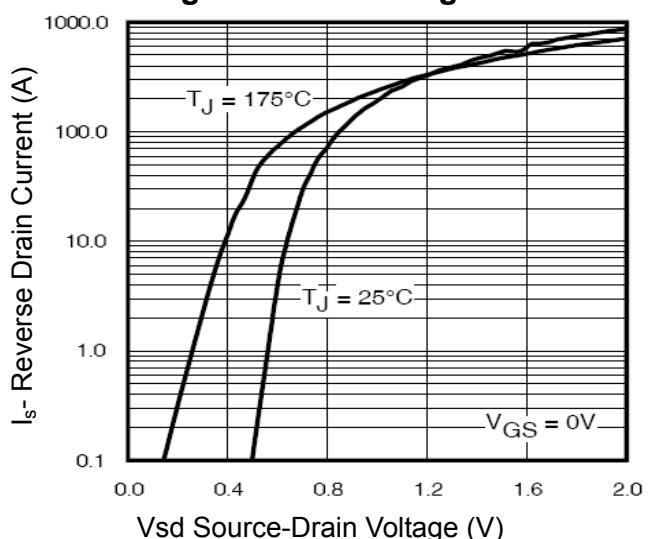


Figure 6 Source- Drain Diode Forward

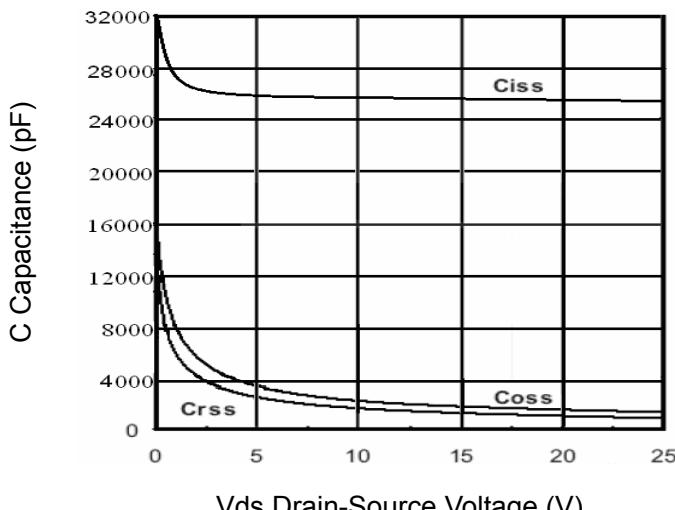


Figure 7 Capacitance vs Vds

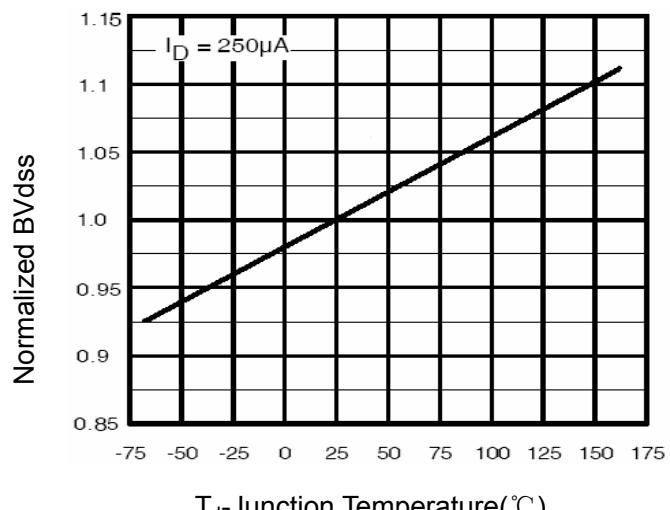


Figure 9 BV_{DSS} vs Junction Temperature

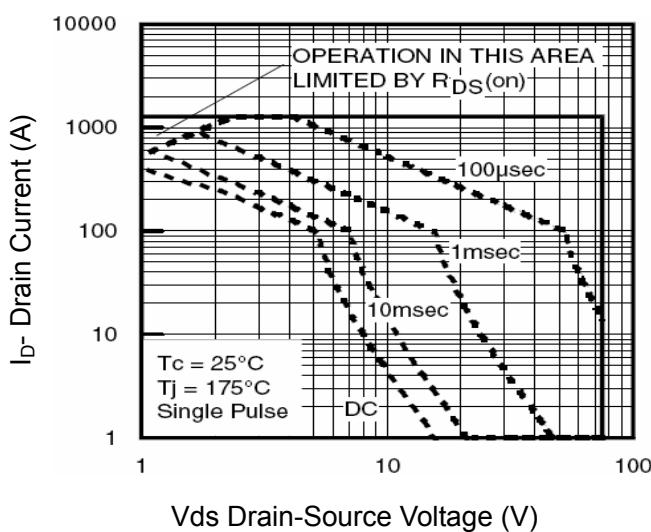


Figure 8 Safe Operation Area

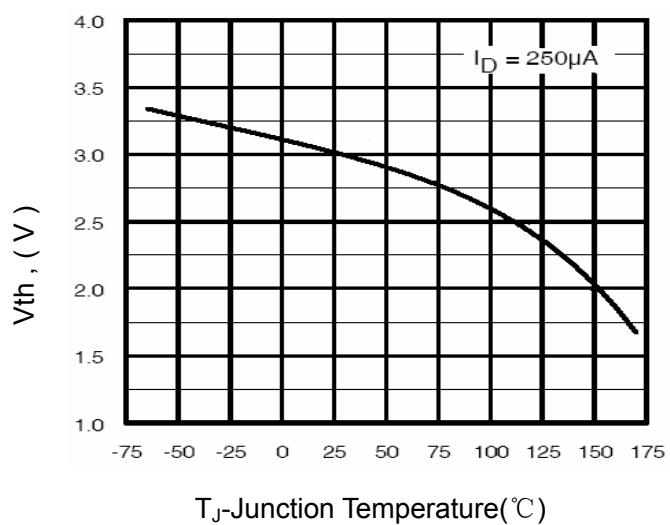
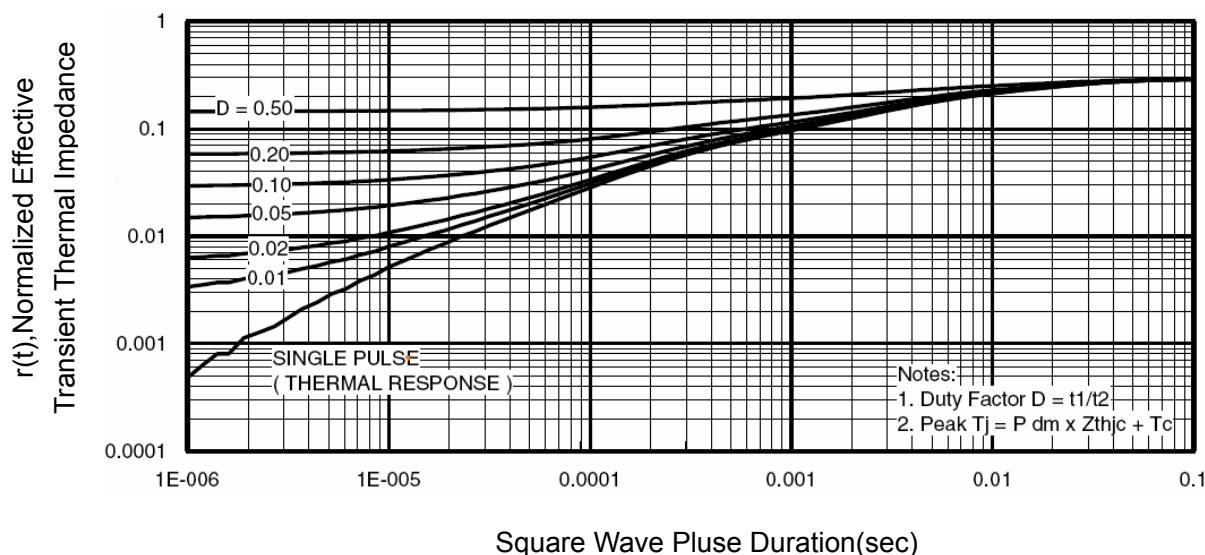
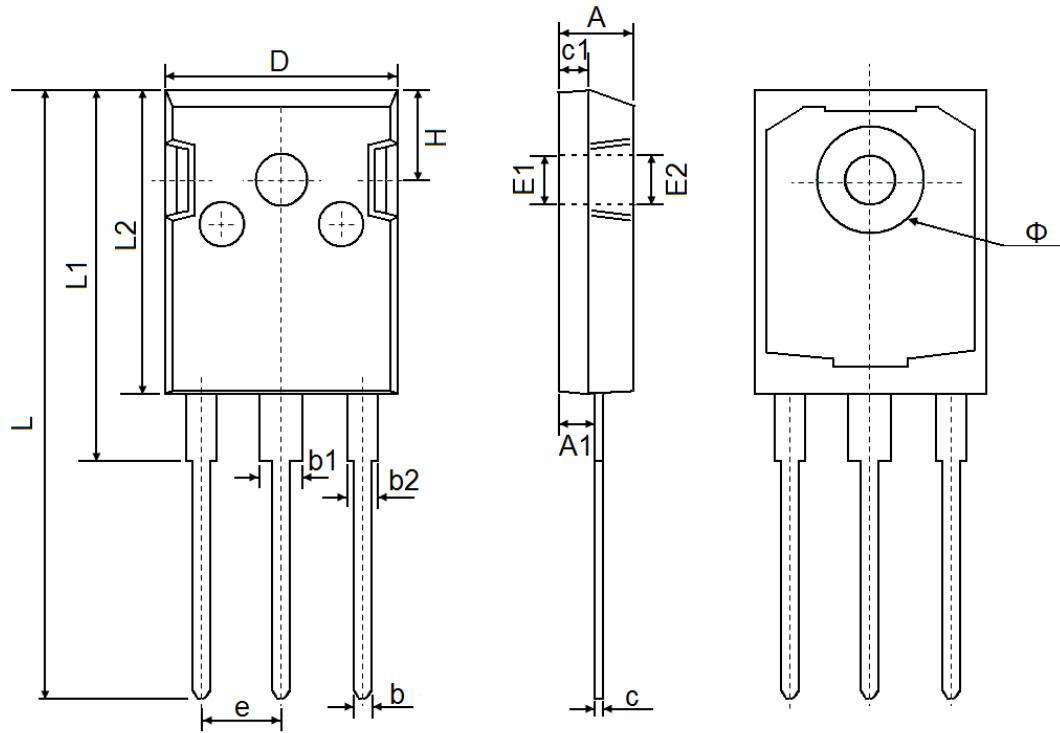


Figure 10 $V_{GS(th)}$ vs Junction Temperature



Square Wave Pulse Duration(sec)

TO-247 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087
c	0.500	0.700	0.020	0.028
c1	1.900	2.100	0.075	0.083
D	15.450	15.750	0.608	0.620
E1	3.500 REF		0.138 REF	
E2	3.600 REF		0.142 REF	
L	40.900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Φ	7.100	7.300	0.280	0.287
e	5.450 TYP		0.215 TYP	
H	5.980 REF		0.235 REF	

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