

## QIAOXIN N-Channel Enhancement Mode Power MOSFET

### Description

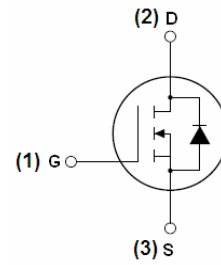
The VCRR0125K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### General Features

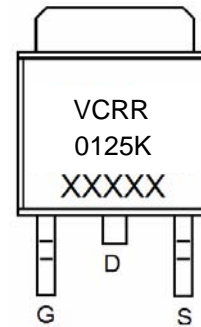
- $V_{DS} = 100V, I_D = 25A$   
 $R_{DS(ON)} < 36m\Omega @ V_{GS}=10V$  (Typ:31 m $\Omega$ )
- Special process technology for high ESD capability
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high  $E_{AS}$
- Excellent package for good heat dissipation

### Application

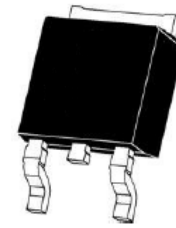
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



Marking and pin assignment



TO-252-2L top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package
VCRR0125K		TO-252-2L

### Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	25	A
$I_D(100^\circ C)$	Drain Current-Continuous( $T_C=100^\circ C$ )	17.6	A
$I_{DM}$	Pulsed Drain Current	70	A
$P_D$	Maximum Power Dissipation	70	W
	Derating factor	0.5	W/ $^\circ C$
$E_{AS}$	Single pulse avalanche energy <sup>(Note 5)</sup>	110	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 175	$^\circ C$

## Thermal Characteristic

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	2	$^{\circ}C/W$
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## Electrical Characteristics ( $T_C=25^{\circ}C$ unless otherwise noted)

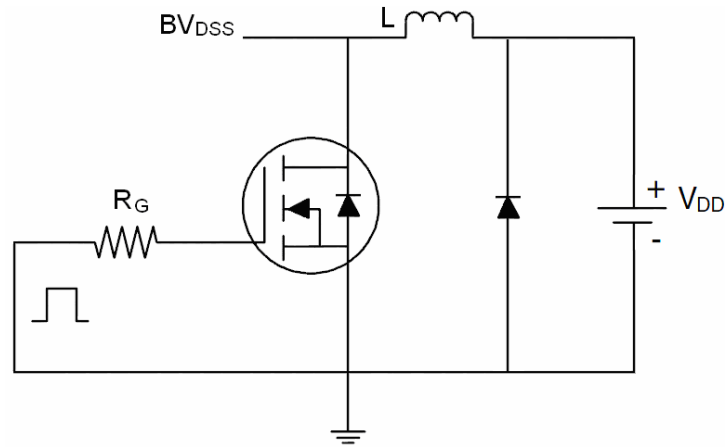
Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	110	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=100V, V_{GS}=0V$	-	-	1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.2	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_D=15A$	-	31	36	m $\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=15A$	-	12	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
$C_{iss}$	Input Capacitance	$V_{DS}=50V, V_{GS}=0V,$ $F=1.0MHz$	-	3000	-	PF
$C_{oss}$	Output Capacitance		-	92	-	PF
$C_{riss}$	Reverse Transfer Capacitance		-	18.3	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=50V, R_L=5\Omega$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	9	-	nS
$t_r$	Turn-on Rise Time		-	9	-	nS
$t_{d(off)}$	Turn-Off Delay Time		-	31	-	nS
$t_f$	Turn-Off Fall Time		-	9	-	nS
$Q_g$	Total Gate Charge	$V_{DS}=50V, I_D=25A,$ $V_{GS}=10V$	-	70.4	-	nC
$Q_{gs}$	Gate-Source Charge		-	9.0	-	nC
$Q_{gd}$	Gate-Drain Charge		-	15.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage <sup>(Note 3)</sup>	$V_{GS}=0V, I_S=25A$	-	-	1.2	V
$I_S$	Diode Forward Current <sup>(Note 2)</sup>	-	-	-	25	A
$t_{rr}$	Reverse Recovery Time	$T_J = 25^{\circ}C, I_F = 25A$	-	34	-	nS
$Q_{rr}$	Reverse Recovery Charge	$di/dt = 100A/\mu s$ <sup>(Note 3)</sup>	-	56	-	nC

### Notes:

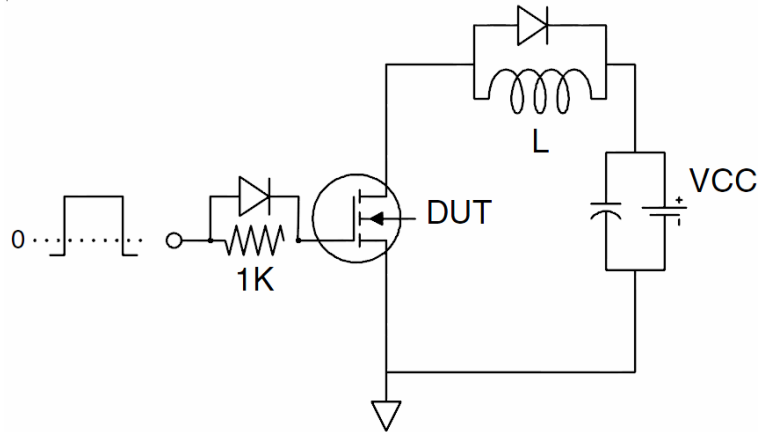
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS Condition :  $T_J=25^{\circ}C, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25\Omega$

**Test Circuit**

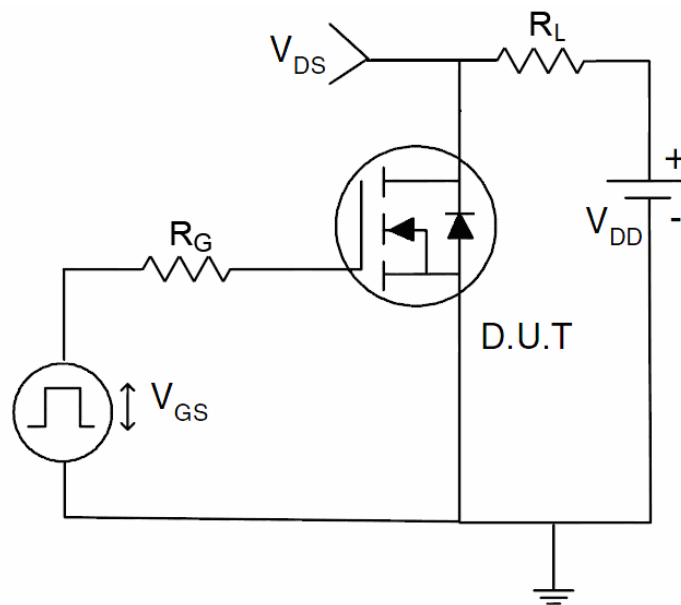
**1) E<sub>AS</sub> Test Circuit**



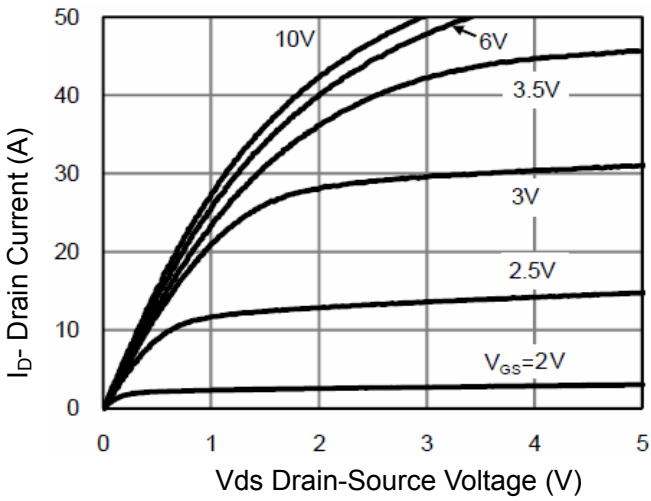
**2) Gate Charge Test Circuit**



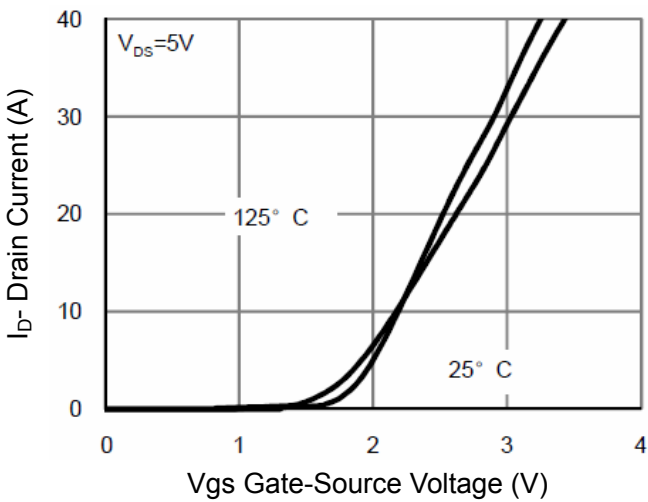
**3) Switch Time Test Circuit**



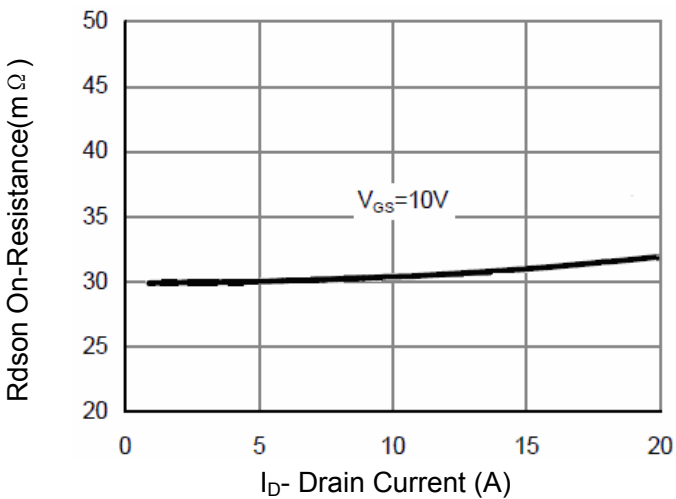
**Typical Electrical and Thermal Characteristics (Curves)**



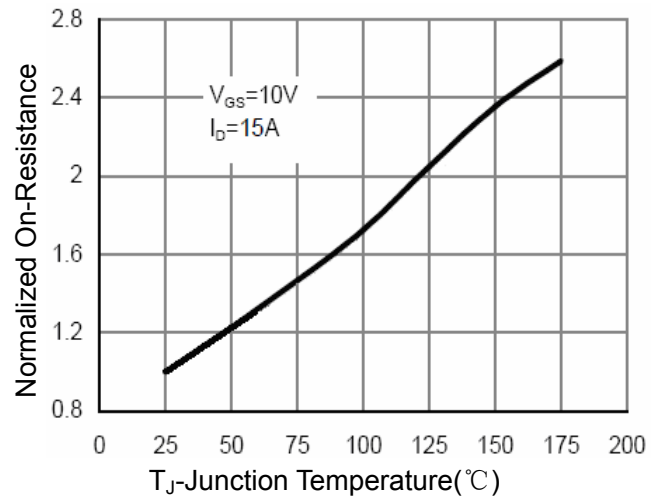
**Figure 1 Output Characteristics**



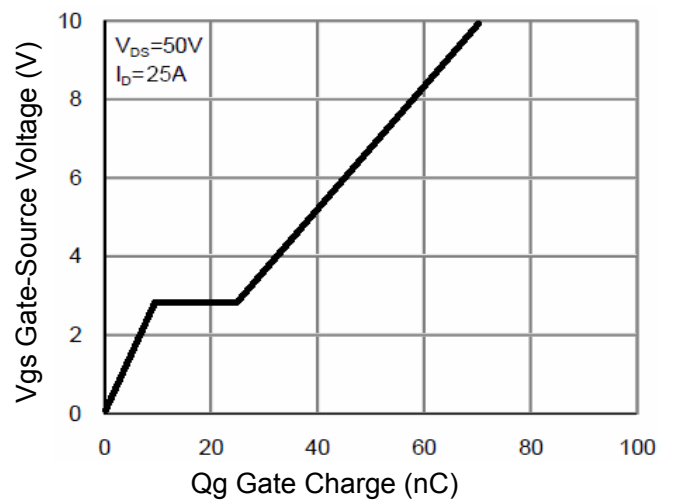
**Figure 2 Transfer Characteristics**



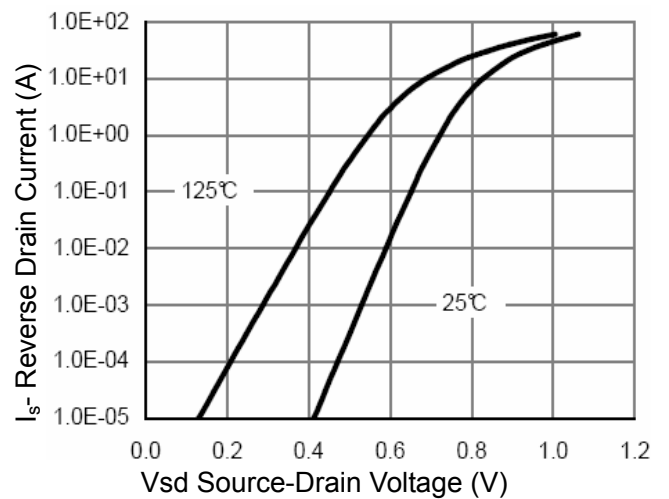
**Figure 3 Rdson- Drain Current**



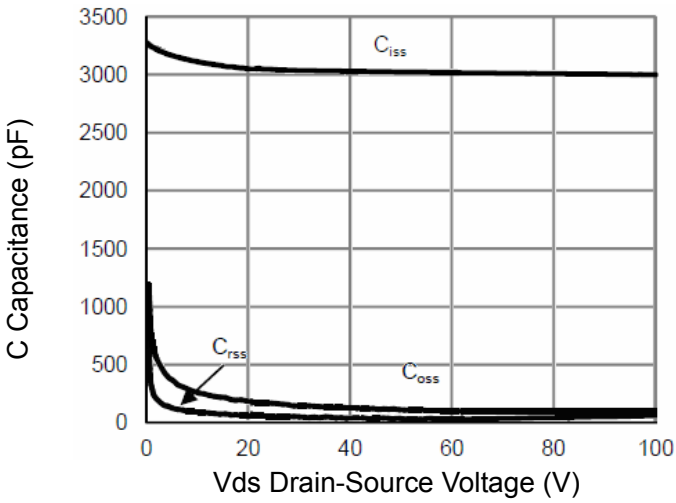
**Figure 4 Rdson-Junction Temperature**



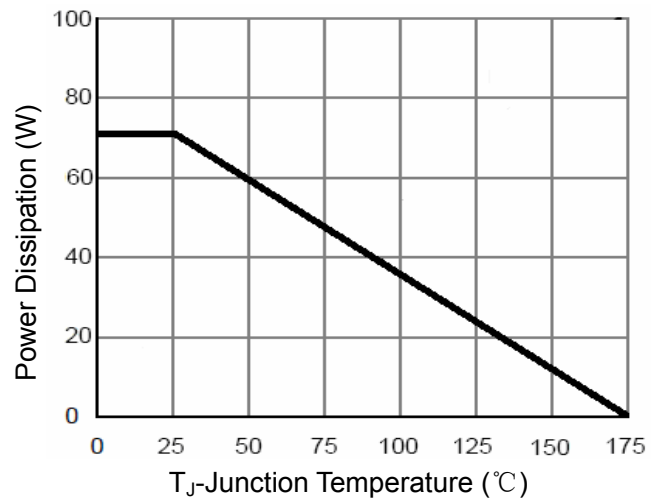
**Figure 5 Gate Charge**



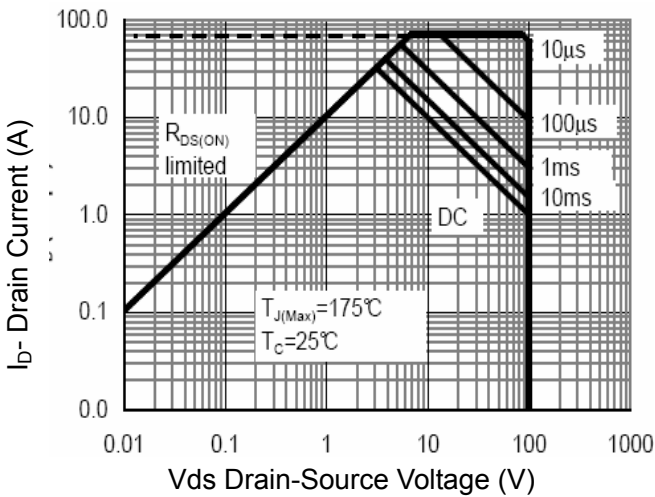
**Figure 6 Source- Drain Diode Forward**



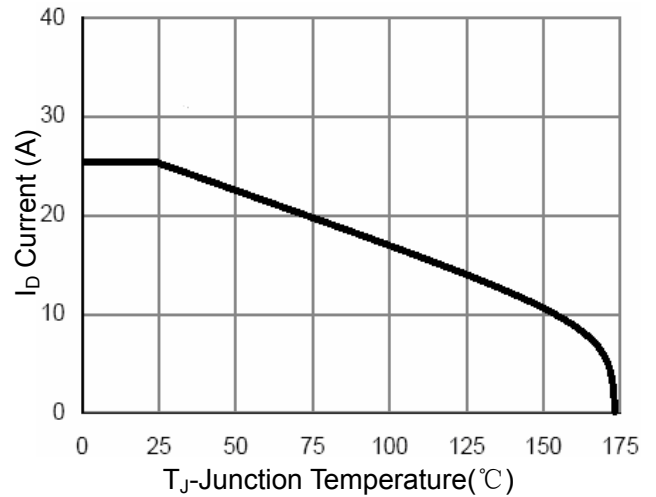
**Figure 7 Capacitance vs Vds**



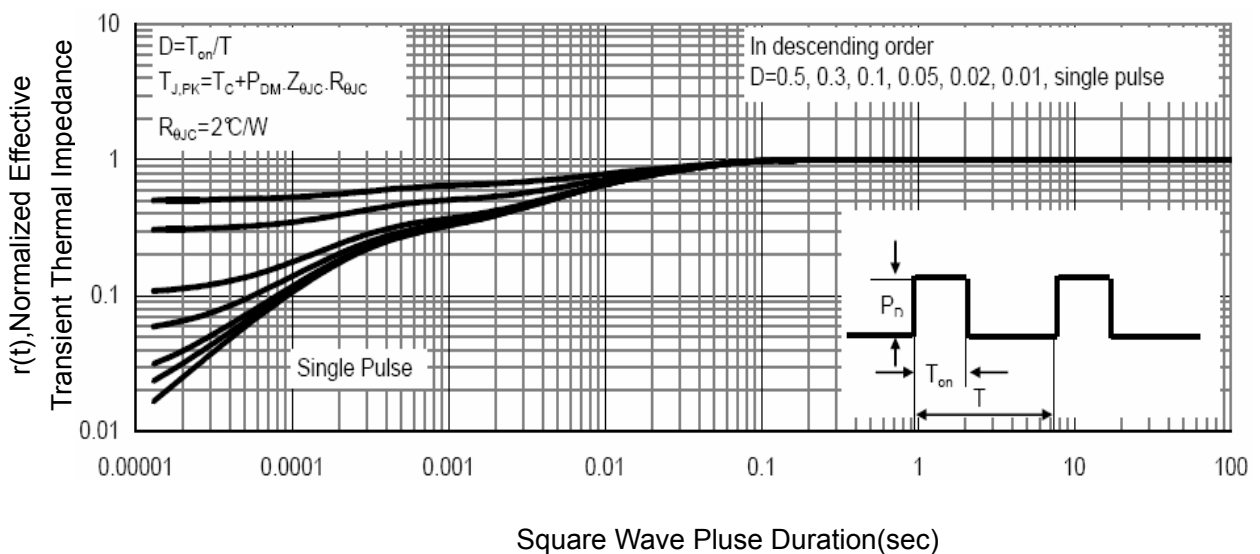
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area**

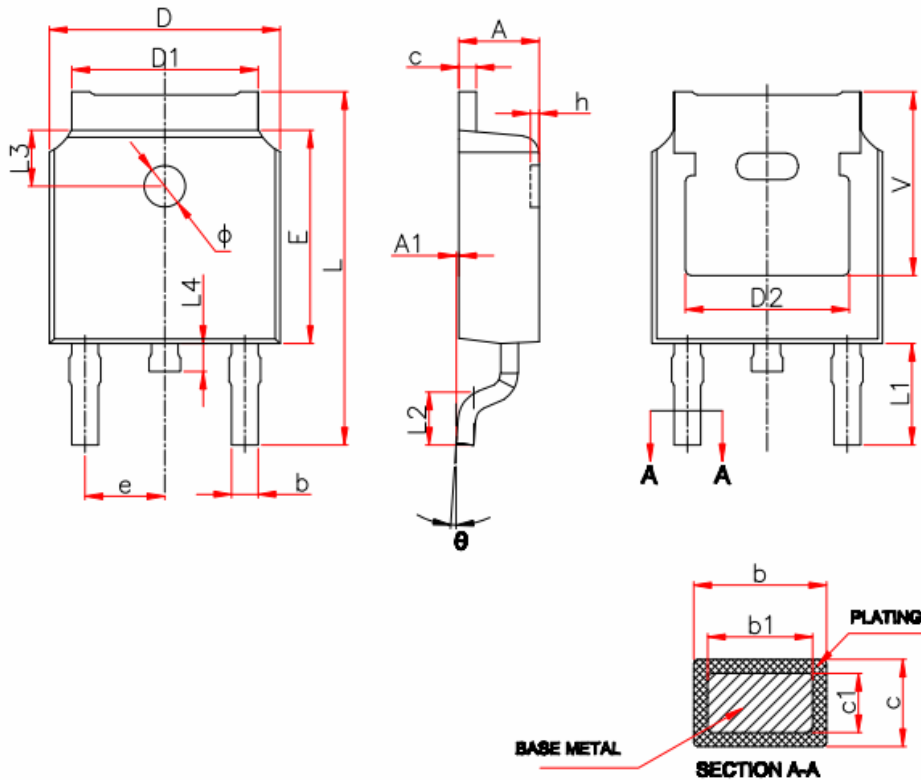


**Figure 10 Id Current- Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

**TO-252 Package Information**



Symbol	Millimeters	
	Min.	Max.
A	2.20	2.40
A1	0.00	0.13
b	0.66	0.86
b1	0.73	0.79
c	0.46	0.58
c1	0.50	0.52
D	6.50	6.70
D1	5.10	5.46
D2	4.83 REF.	
E	6.00	6.20
e	2.19	2.39
L	9.80	10.40
L1	2.90 REF.	
L2	1.40	1.70
L3	1.60 REF.	
L4	0.60	1.00
φ	1.10	1.30
θ	0°	8°

### **Attention**

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