

QIAOXIN N-Channel **Super Trench** Power MOSFET

Description

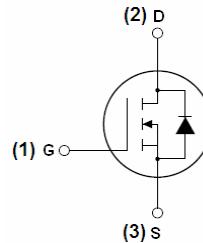
The VCRR0178A uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

- $V_{DS} = 100V, I_D = 78A$
- $R_{DS(ON)} = 7.2m\Omega$ (typical) @ $V_{GS}=10V$
- $R_{DS(ON)} = 9.5m\Omega$ (typical) @ $V_{GS}=4.5V$
- Excellent gate charge $\times R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

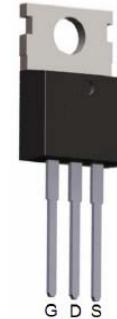
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VCRR0178A	VCRR0178A	TO-220-3L	-	-	-

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	78	A
Drain Current-Continuous($T_C=100^\circ C$)	$I_D (100^\circ C)$	60	A
Pulsed Drain Current	I_{DM}	320	A
Maximum Power Dissipation	P_D	125	W
Derating factor		0.83	W/°C
Single pulse avalanche energy ^(Note 5)	E_{AS}	320	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	1.2	°C/W
--	------------------	-----	------

Electrical Characteristics (T_C=25°C unless otherwise noted)

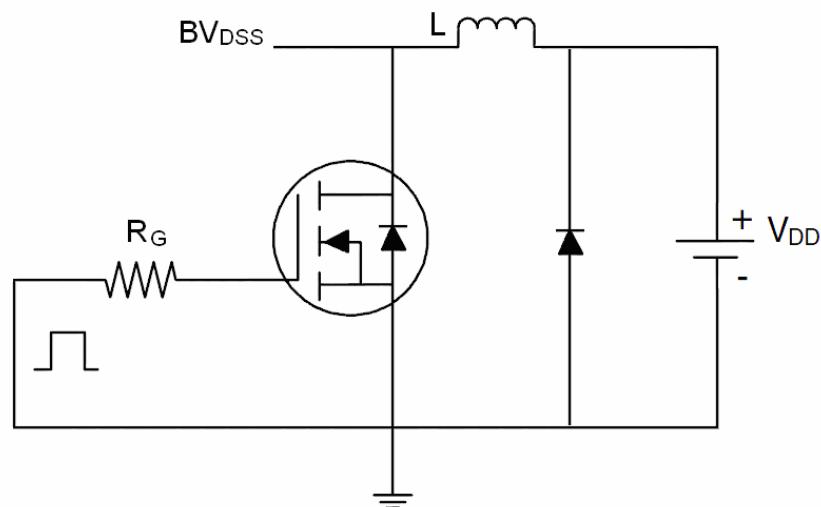
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.2	1.7	2.2	V
Drain-Source On-State Resistance	R _{Ds(ON)}	V _{GS} =10V, I _D =39A	-	7.2	8.5	mΩ
		V _{GS} =4.5V, I _D =39A	-	9.5	12	mΩ
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =39A	40	-	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, F=1.0MHz	-	4200	5480	PF
Output Capacitance	C _{oss}		-	354	425	PF
Reverse Transfer Capacitance	C _{rss}		-	23	30	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, I _D =39A V _{GS} =10V, R _G =4.7Ω	-	15	-	nS
Turn-on Rise Time	t _r		-	10	-	nS
Turn-Off Delay Time	t _{d(off)}		-	41	-	nS
Turn-Off Fall Time	t _f		-	6	-	nS
Total Gate Charge	Q _g	V _{DS} =50V, I _D =39A, V _{GS} =10V	-	65		nC
Gate-Source Charge	Q _{gs}		-	15.3		nC
Gate-Drain Charge	Q _{gd}		-	9		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _S =78A	-		1.2	V
Diode Forward Current ^(Note 2)	I _S		-	-	78	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S di/dt = 100A/μs ^(Note 3)	-	101		nS
Reverse Recovery Charge	Q _{rr}		-	193		nC

Notes:

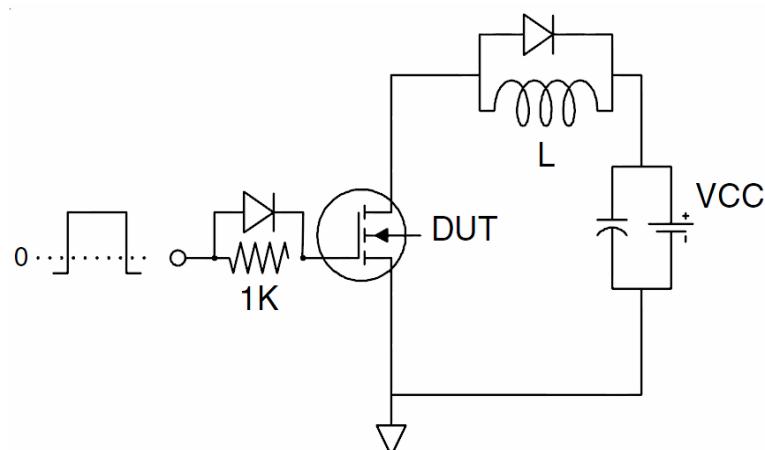
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition : T_j=25°C, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25Ω

Test Circuit

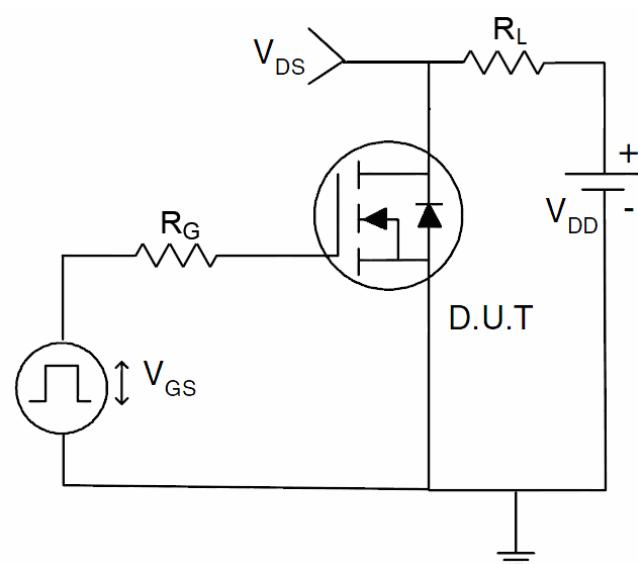
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

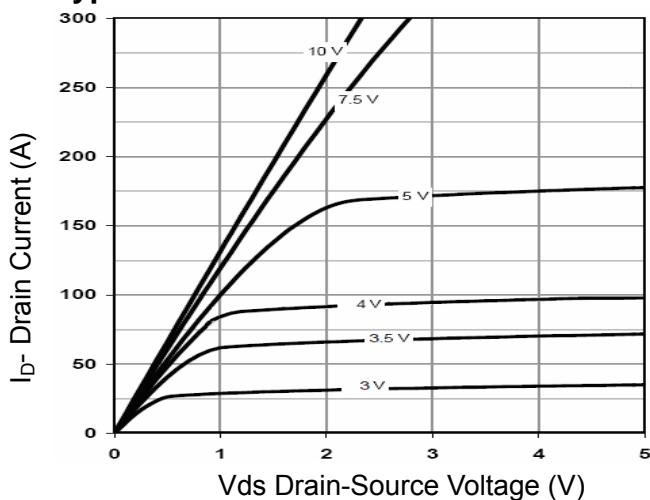


Figure 1 Output Characteristics

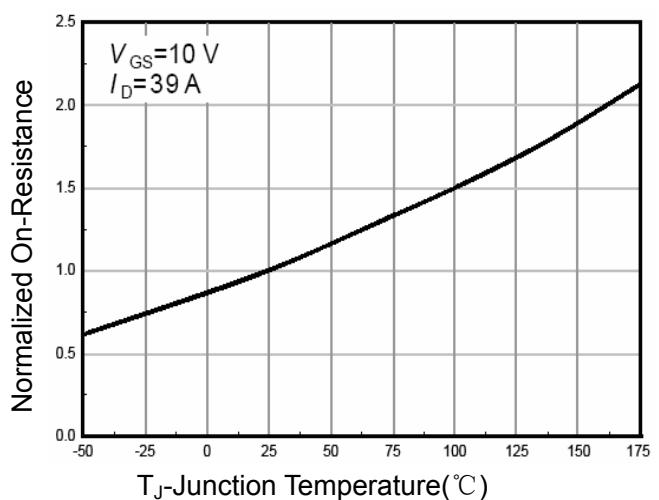


Figure 4 Rdson-JunctionTemperature

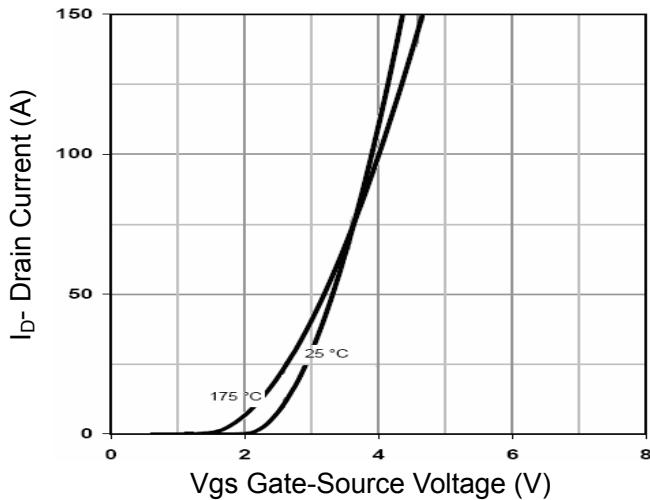


Figure 2 Transfer Characteristics

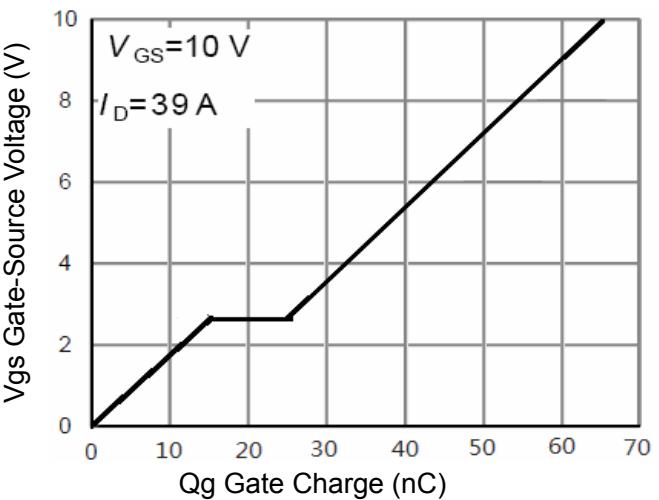


Figure 5 Gate Charge

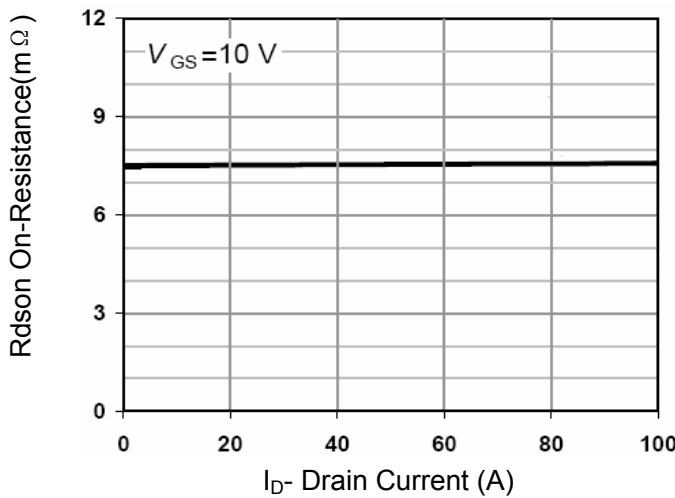


Figure 3 Rdson- Drain Current

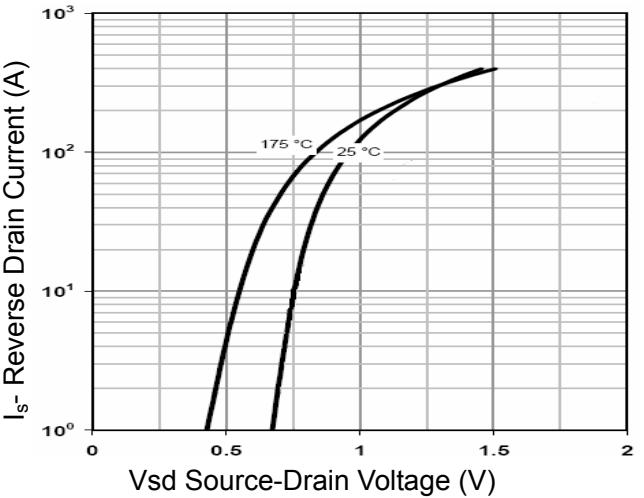


Figure 6 Source- Drain Diode Forward

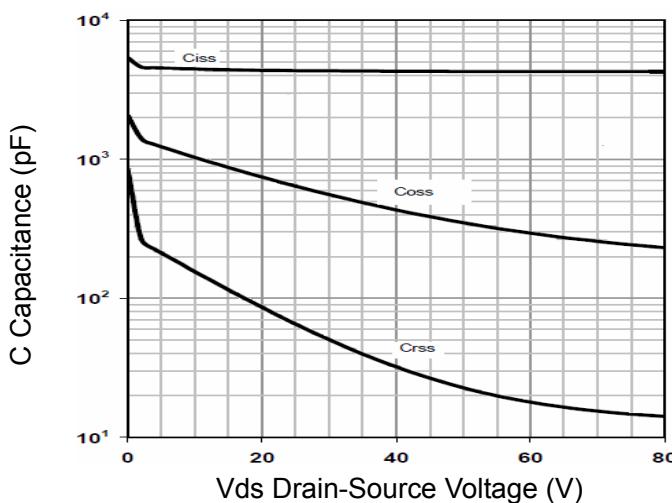


Figure 7 Capacitance vs Vds

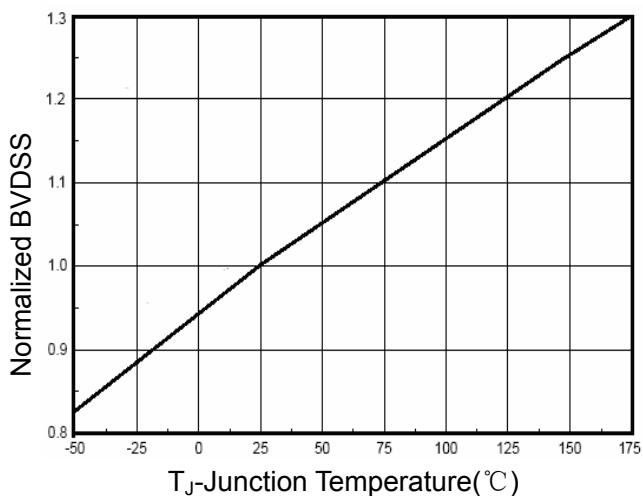


Figure 9 BV_{DSS} vs Junction Temperature

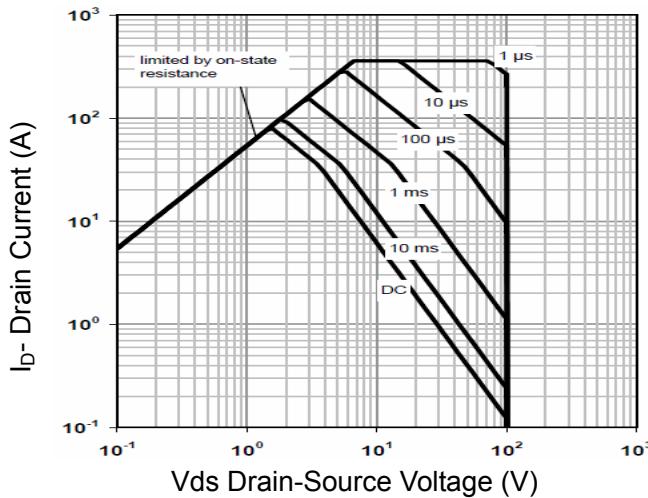


Figure 8 Safe Operation Area

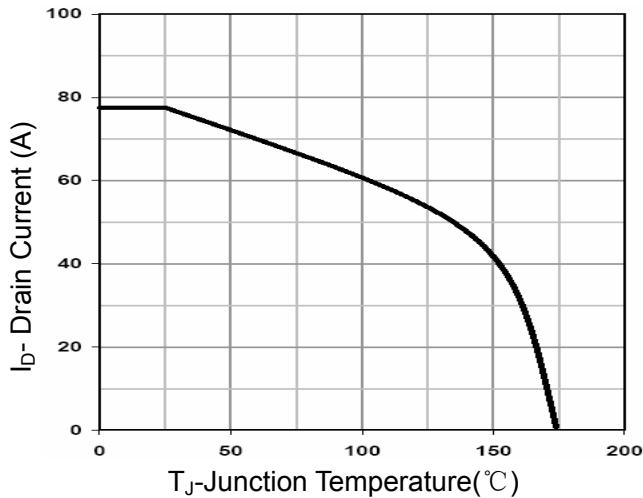


Figure 10 Current De-rating

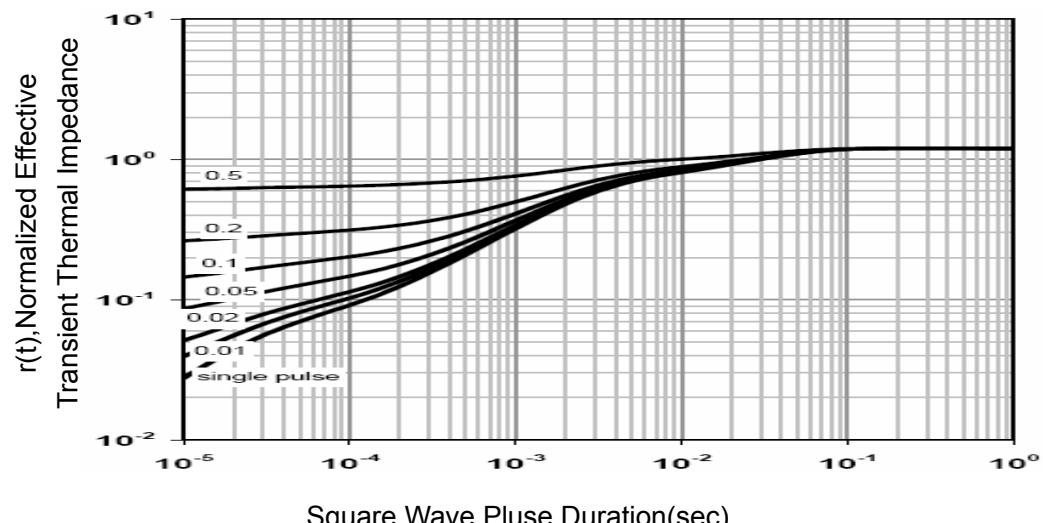


Figure 11 Normalized Maximum Transient Thermal Impedance

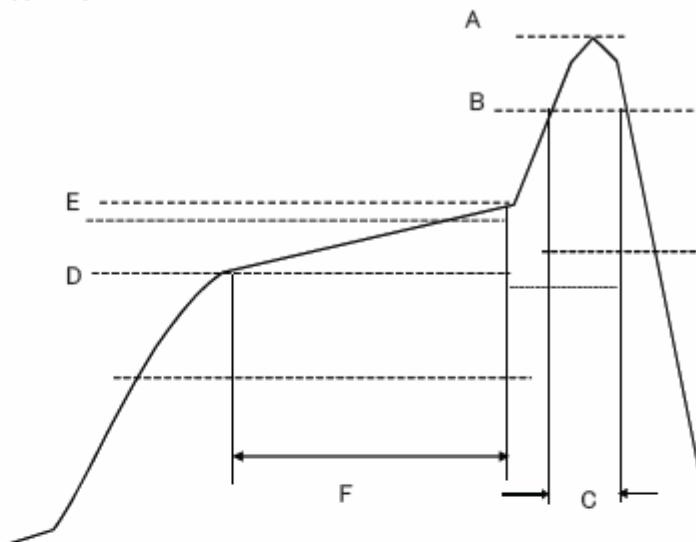
Reflow Curve

The Guarantee Letter of Parts Heat Resistance

新洁能表贴封装产品，有在以下耐热温度条件下保证的可能，建议追记到有关零件承认书的零件耐热保证项目中
 (The SMD components of NCE Power have the ability of heat resistance marked below. Please add the information to corresponded items of heat resistance in Supplier Specification Submission.)

[REFLOW溫度PROFILE]
 (REFLOW TEMPERATURE PROFILE)

测试POINT : 焊接部 - part表面
A: 260±5/-0 °C
B: 217°C
C: 60~150 sec
D: 150°C
E: 200°C
F: 90±10 sec

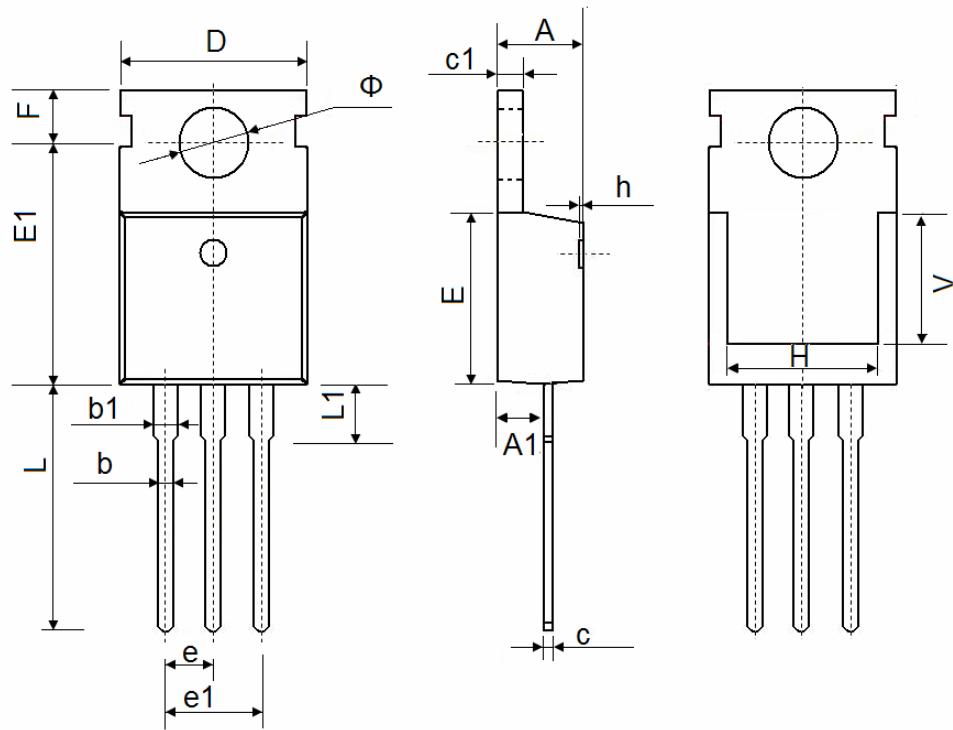


reflow条件(次数等) (Reflow condition (times))	<input checked="" type="checkbox"/> 可用以上PROFILE进行3次 (can use the above profile for two times) <input type="checkbox"/> 不可REFLOW (can not Reflow)		
开封后的吸湿保证条件 (Damp resistance after opening)	<input type="checkbox"/> 无限制 (保管条件 store condition: 30°C 70%RH以下) <input checked="" type="checkbox"/> 开封后 30°C 60%RH168H→REFLOW (after opening)		
手焊耐热 (Soldering iron)	350±10°C 5+1/-0 sec	flow耐热 (flow heat-resistant)	270±3°C 10+1/-0 sec
Pb含有状况 (Pb content status)	零件外部 (external) 零件内部 (inside) 电极镀层的组成 (Composition of lead cladding) 无铅区分 (Pb-free manage)	<input checked="" type="checkbox"/> Pb完全无使用 Pb-free <input type="checkbox"/> Pb有使用其位置 (无铅化预定 年 月) Pb used in location (realize Pb-free year month) <input type="checkbox"/> Pb完全无使用 Pb-free <input checked="" type="checkbox"/> Pb有使用其位置 (内部电极含有Pb, 无铅化预定 年 月) Pb used in the inside electrodes (realize Pb-free year month) <input checked="" type="checkbox"/> Sn, <input type="checkbox"/> Sn-Cu, <input type="checkbox"/> Sn-Ag, <input type="checkbox"/> Sn-Bi, <input type="checkbox"/> 其他(other)() <input type="checkbox"/> Sn-Pb (无铅化预定 年 月) (realize Pb-free year month) <input type="checkbox"/> 料号变更 (无铅零件料号: ABC12345) P/N changed (Pb-free P/N:) <input checked="" type="checkbox"/> 料号不变,自然切换 (切换时间点: 年 月 日) P/N not changed,switch naturally (switch time: year month date)	

Solder Dip

260°C /10Sec Whole body

TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150

Attention

QIAOXIN assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all QIAOXIN products described or contained herein. QIAOXIN products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. QIAOXIN reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.