

QIAOXIN N-Channel **Super Trench** Power MOSFET

**Description**

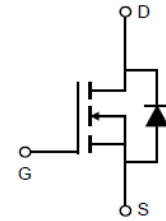
The VCRRP1260F uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

**General Features**

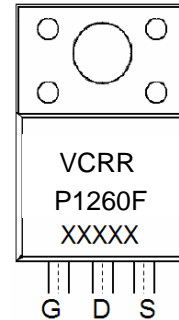
- $V_{DS} = 120V, I_D = 60A$   
 $R_{DS(ON)} = 10m\Omega$ (typical) @  $V_{GS} = 10V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

**Application**

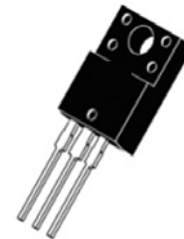
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic diagram



Marking and pin assignment



TO-220F top view

**Package Marking and Ordering Information**

| Device Marking | Device | Device Package |
|----------------|--------|----------------|
| VCRRP1260F     |        | TO-220F        |

**Absolute Maximum Ratings ( $T_C = 25^\circ C$  unless otherwise noted)**

| Parameter   | Symbol             | Limit      | Unit |
|---|--------------------|------------|------|
| Drain-Source Voltage                              | $V_{DS}$           | 120        | V    |
| Gate-Source Voltage                               | $V_{GS}$           | $\pm 20$   | V    |
| Drain Current-Continuous                          | $I_D$              | 60         | A    |
| Drain Current-Continuous( $T_C = 100^\circ C$ )   | $I_D(100^\circ C)$ | 42.4       | A    |
| Pulsed Drain Current                              | $I_{DM}$           | 240        | A    |
| Maximum Power Dissipation                         | $P_D$              | 35         | W    |
| Derating factor                                   |                    | 0.23       | W/°C |
| Single pulse avalanche energy <sup>(Note 5)</sup> | $E_{AS}$           | 290        | mJ   |
| Operating Junction and Storage Temperature Range  | $T_J, T_{STG}$     | -55 To 175 | °C   |

**Thermal Characteristic**

|  |                 |     |      |
|--|-----------------|-----|------|
| Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup> | $R_{\theta JC}$ | 4.3 | °C/W |
|--|-----------------|-----|------|

### Electrical Characteristics ( $T_C=25^{\circ}\text{C}$ unless otherwise noted)

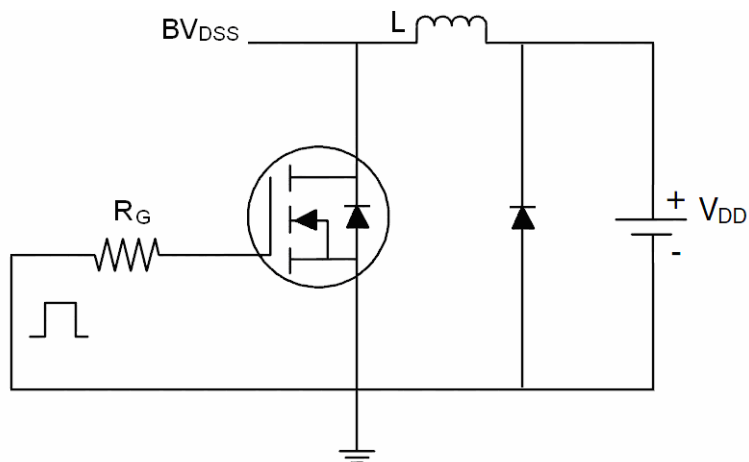
| Parameter                                 | Symbol       | Condition  | Min | Typ  | Max       | Unit       |
|---|--------------|--|-----|------|-----------|------------|
| <b>Off Characteristics</b>                |              |  |     |      |           |            |
| Drain-Source Breakdown Voltage            | $BV_{DSS}$   | $V_{GS}=0V, I_D=250\mu A$  | 120 |      | -         | V          |
| Zero Gate Voltage Drain Current           | $I_{DSS}$    | $V_{DS}=120V, V_{GS}=0V$   | -   | -    | 1         | $\mu A$    |
| Gate-Body Leakage Current                 | $I_{GSS}$    | $V_{GS}=\pm 20V, V_{DS}=0V$  | -   | -    | $\pm 100$ | nA         |
| <b>On Characteristics</b> (Note 3)        |              |  |     |      |           |            |
| Gate Threshold Voltage                    | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$  | 2   | 3    | 4         | V          |
| Drain-Source On-State Resistance          | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=30A$  | -   | 10   | 11        | m $\Omega$ |
| Forward Transconductance                  | $g_{FS}$     | $V_{DS}=5V, I_D=30A$   | -   | 40   | -         | S          |
| <b>Dynamic Characteristics</b> (Note 4)   |              |  |     |      |           |            |
| Input Capacitance                         | $C_{iss}$    | $V_{DS}=60V, V_{GS}=0V,$<br>$F=1.0\text{MHz}$                          | -   | 2500 | -         | PF         |
| Output Capacitance                        | $C_{oss}$    |  | -   | 273  | -         | PF         |
| Reverse Transfer Capacitance              | $C_{rss}$    |  | -   | 27   | -         | PF         |
| <b>Switching Characteristics</b> (Note 4) |              |  |     |      |           |            |
| Turn-on Delay Time                        | $t_{d(on)}$  | $V_{DD}=50V, I_D=30A$<br>$V_{GS}=10V, R_G=3\Omega$                     | -   | 11   | -         | nS         |
| Turn-on Rise Time                         | $t_r$        |  | -   | 7.5  | -         | nS         |
| Turn-Off Delay Time                       | $t_{d(off)}$ |  | -   | 26   | -         | nS         |
| Turn-Off Fall Time                        | $t_f$        |  | -   | 4    | -         | nS         |
| Total Gate Charge                         | $Q_g$        | $V_{DS}=60V, I_D=30A,$<br>$V_{GS}=10V$                                 | -   | 37   |           | nC         |
| Gate-Source Charge                        | $Q_{gs}$     |  | -   | 14   |           | nC         |
| Gate-Drain Charge                         | $Q_{gd}$     |  | -   | 8    |           | nC         |
| <b>Drain-Source Diode Characteristics</b> |              |  |     |      |           |            |
| Diode Forward Voltage (Note 3)            | $V_{SD}$     | $V_{GS}=0V, I_S=60A$   | -   |      | 1.2       | V          |
| Diode Forward Current (Note 2)            | $I_S$        |  | -   | -    | 60        | A          |
| Reverse Recovery Time                     | $t_{rr}$     | $T_J = 25^{\circ}\text{C}, I_F = I_S$<br>$di/dt = 100A/\mu s$ (Note 3) | -   | 58   |           | nS         |
| Reverse Recovery Charge                   | $Q_{rr}$     |  | -   | 149  |           | nC         |

#### Notes:

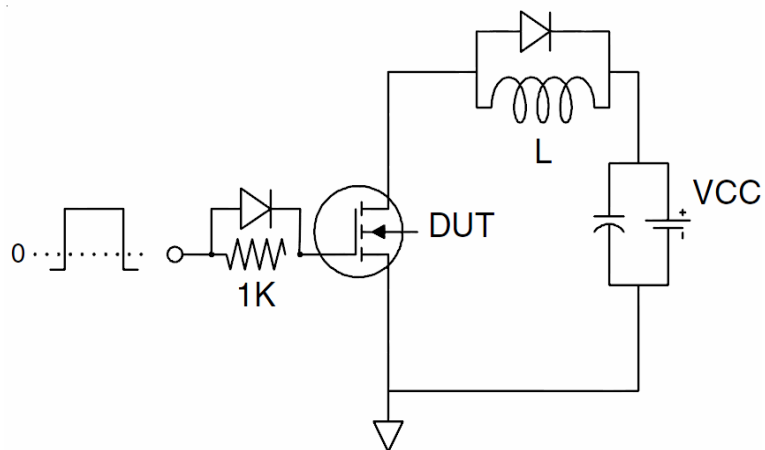
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=0.5\text{mH}, R_G=25\Omega$

## Test Circuit

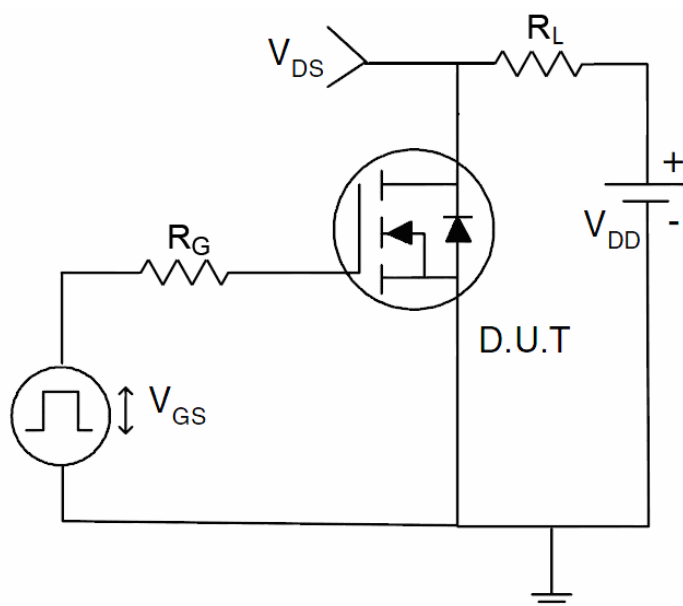
### 1) $E_{AS}$ test Circuit



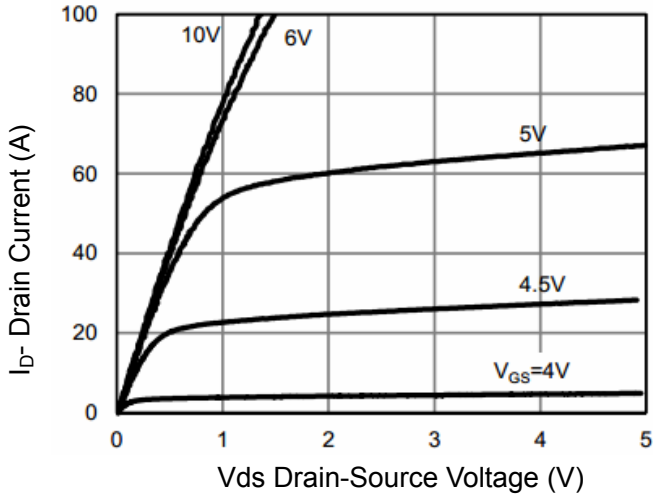
### 2) Gate charge test Circuit



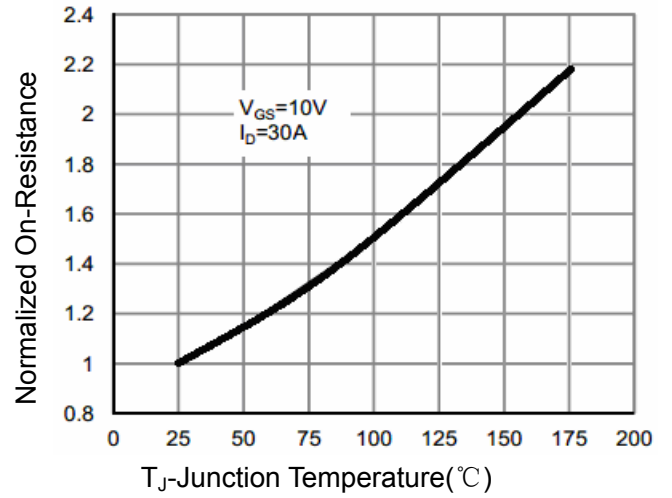
### 3) Switch Time Test Circuit



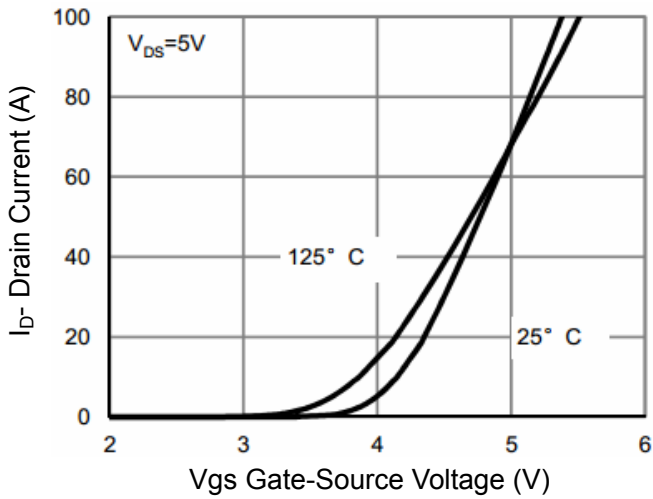
**Typical Electrical and Thermal Characteristics**



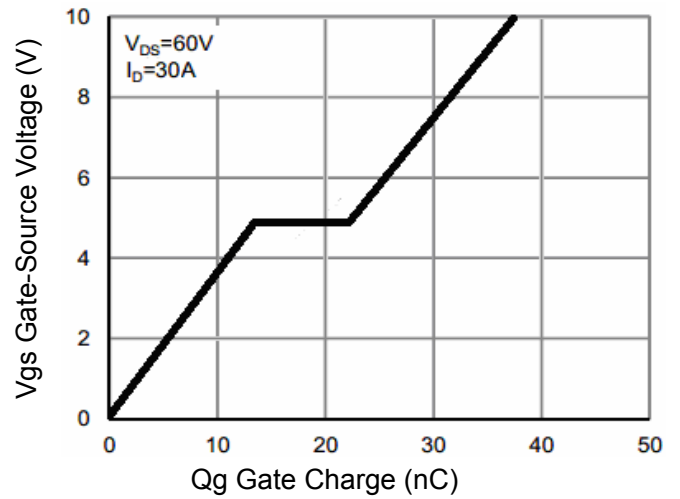
**Figure 1 Output Characteristics**



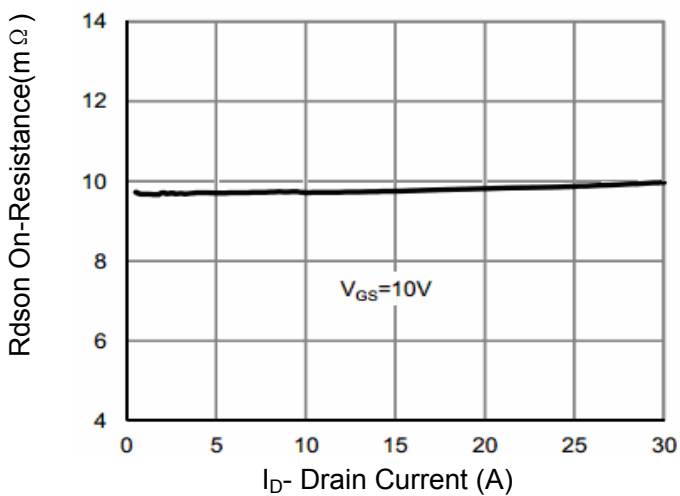
**Figure 4 Rdson-Junction Temperature**



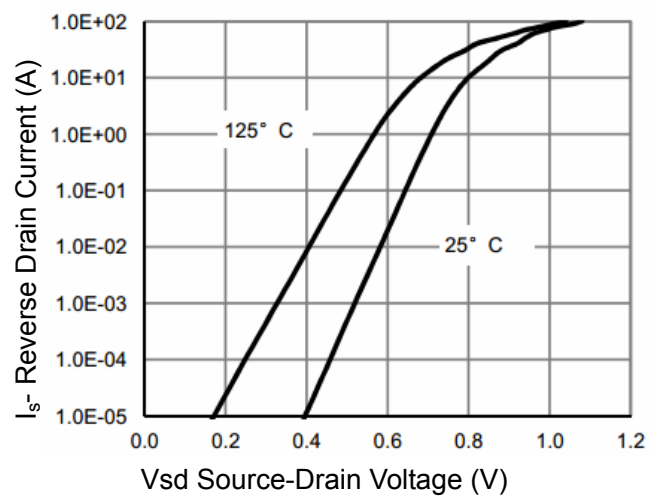
**Figure 2 Transfer Characteristics**



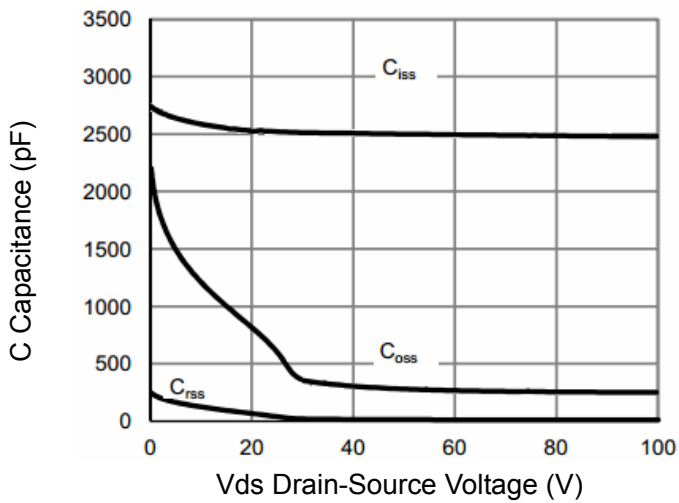
**Figure 5 Gate Charge**



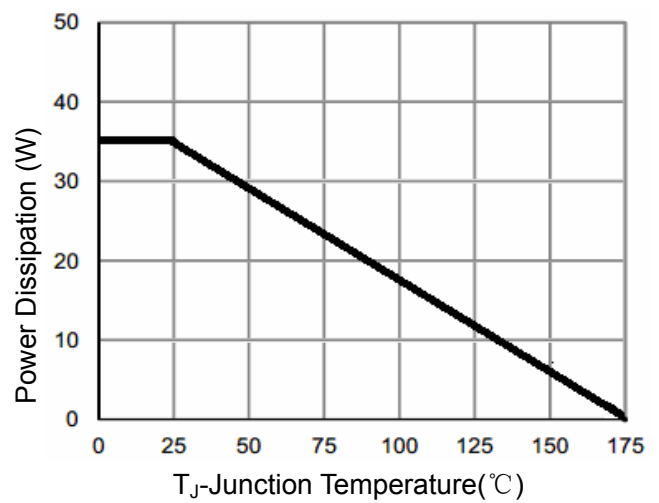
**Figure 3 Rdson- Drain Current**



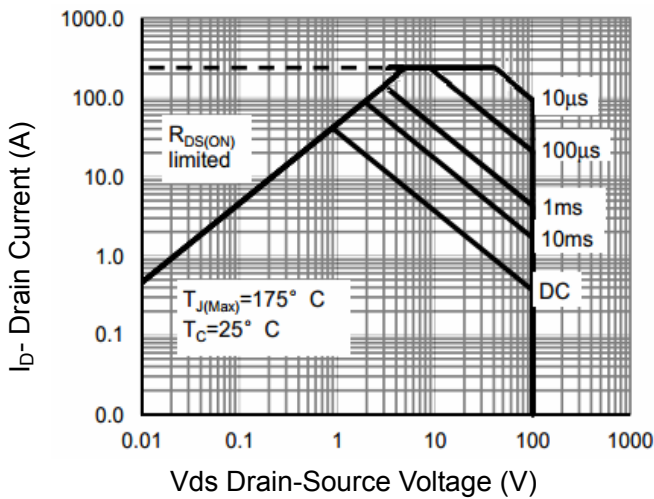
**Figure 6 Source- Drain Diode Forward**



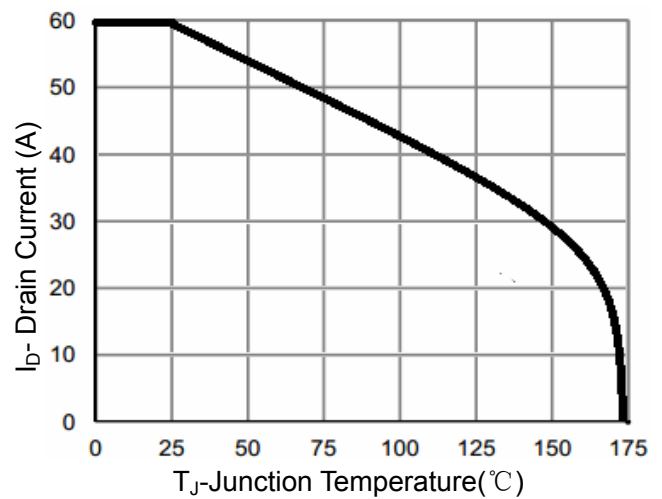
**Figure 7 Capacitance vs Vds**



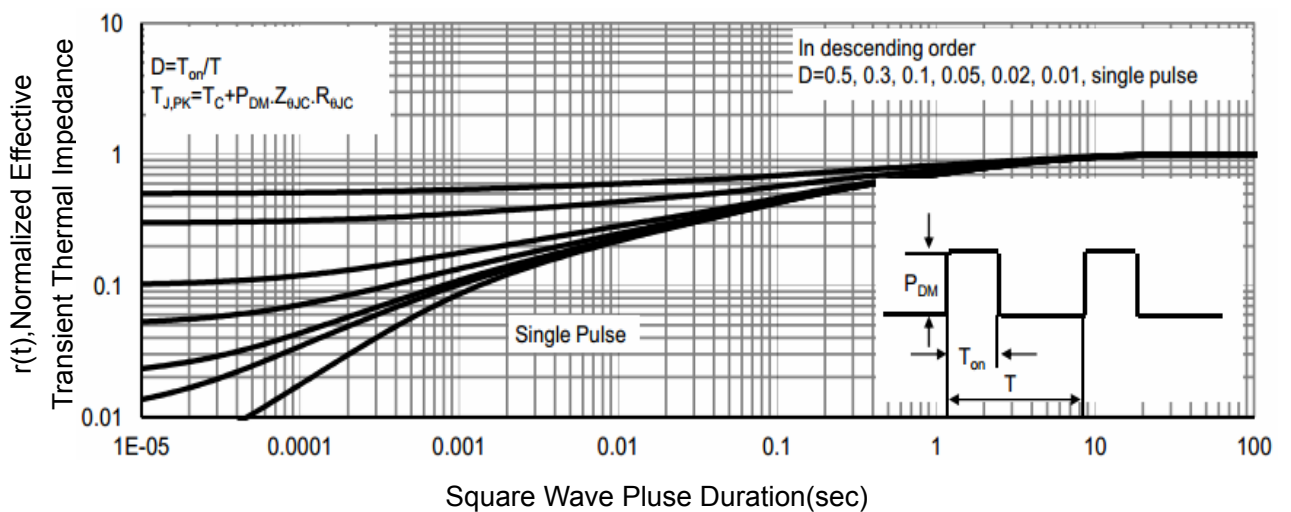
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area**

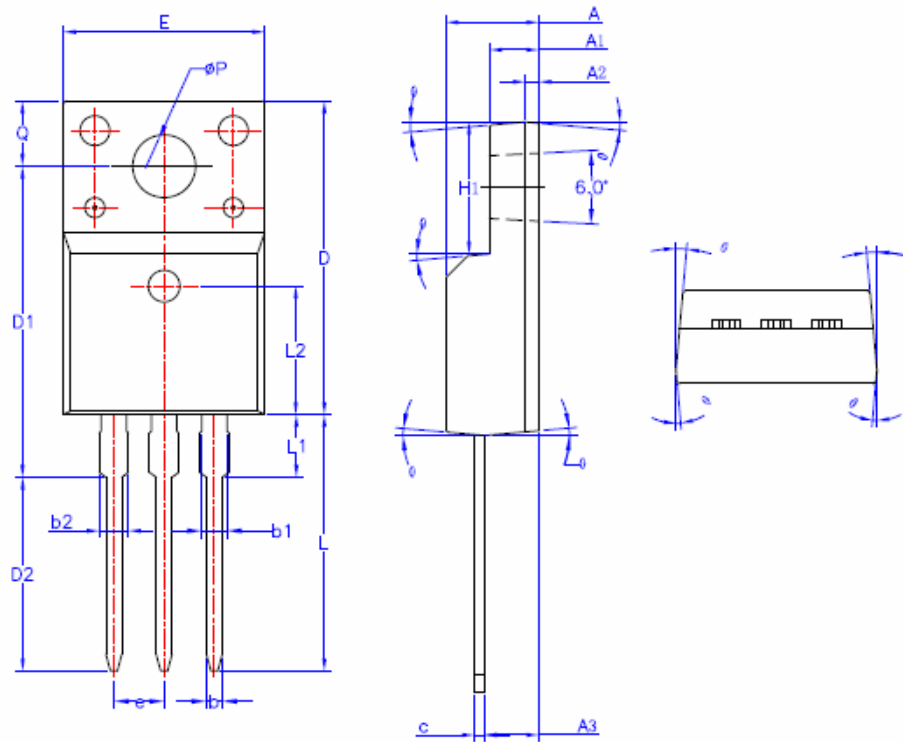


**Figure 10 Current De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

**TO-220F Package Information**



| SYMBOL     | MIN      | NOM   | MAX   |
|------------|----------|-------|-------|
| A          | 4.50     | 4.70  | 4.83  |
| A1         | 2.34     | 2.54  | 2.74  |
| A2         | 0.70 REF |       |       |
| A3         | 2.56     | 2.76  | 2.93  |
| b          | 0.70     | —     | 0.90  |
| b1         | 1.18     | —     | 1.38  |
| b2         | —        | —     | 1.47  |
| c          | 0.45     | 0.50  | 0.60  |
| D          | 15.67    | 15.87 | 16.07 |
| D1         | 15.55    | 15.75 | 15.95 |
| D2         | 9.60     | 9.80  | 10.0  |
| E          | 9.96     | 10.16 | 10.36 |
| e          | 2.54BSC  |       |       |
| H1         | 6.48     | 6.68  | 6.88  |
| L          | 12.68    | 12.98 | 13.28 |
| L1         | —        | —     | 3.50  |
| L2         | 6.50REF  |       |       |
| $\phi P$   | 3.08     | 3.18  | 3.28  |
| Q          | 3.20     | —     | 3.40  |
| $\theta 1$ | 1°       | 3°    | 5°    |

### Attention

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