VCRR035N60AK

QIAOXIN N-Channel Super Trench II Power MOSFET

Description

The VCRR 035N60AK uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

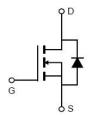
Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

- V_{DS} =60V, I_D =130A $R_{DS(ON)}$ =2.8m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =3.5m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating





Schematic Diagram

Package Marking and Ordering Information

1	Device Marking	Device	Device Package
	VCRR 035N60AK		TO-252-2L

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous (Silicon Limited)	I _D	130	Α
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	100	Α
Pulsed Drain Current	I _{DM}	520	Α
Maximum Power Dissipation	P _D	140	W
Derating factor		0.93	W/℃
Single pulse avalanche energy (Note 1)	Eas	520	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	Rejc	1.07	°C/W
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Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	60		-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	1.0	1.7	2.4	V	
Dunin Course On Chata Begintanes	Б	V _{GS} =10V, I _D =65A	-	2.8	3.5	mΩ	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =65A	-	3.5	4.5	mΩ	
Forward Transconductance	g FS	V _{DS} =5V,I _D =65A	40	-	-	S	
Dynamic Characteristics							
Input Capacitance	C _{lss}	V 20V/V 0V	-	4000	-	PF	
Output Capacitance	Coss	$V_{DS}=30V, V_{GS}=0V,$	-	605	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	44	-	PF	
Switching Characteristics (Note 2)							
Turn-on Delay Time	t _{d(on)}		-	11	-	nS	
Turn-on Rise Time	tr	V_{DD} =30 V , I_{D} =65 A	-	5	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =4.7 Ω	-	49	-	nS	
Turn-Off Fall Time	t _f	-		10	-	nS	
Total Gate Charge	Qg	\/ 00\/1 05A	-	73		nC	
Gate-Source Charge	Qgs	V_{DS} =30 V , I_{D} =65 A ,	-	12.5		nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =10V		11		nC	
Drain-Source Diode Characteristics	· '		•			•	
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =65A	-		1.2	V	
Diode Forward Current	Forward Current I _S		-	-	130	Α	
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S	-	48		nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/μs	-	60		nC	

Notes:

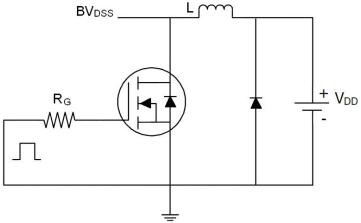
- 1. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω
- 2. Guaranteed by design, not subject to production
- 3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of T_{J(MAX)}=175°C. The SOA curve provides a single pulse rating.

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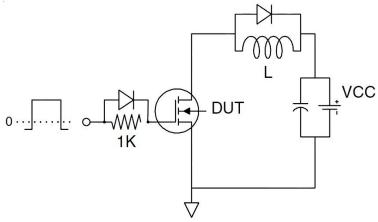


Test Circuit

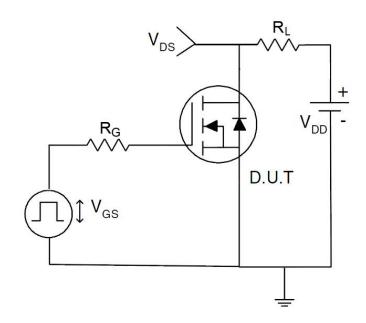
1) E_{AS} test Circuit



2) Gate charge test Circuit



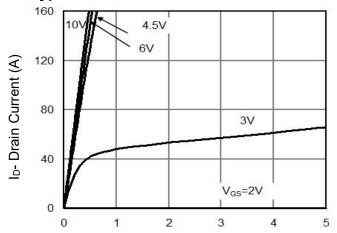
3) Switch Time Test Circuit



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Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

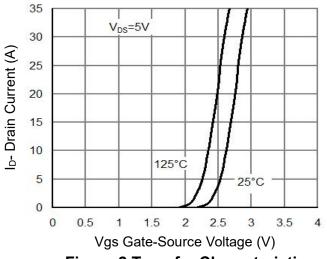


Figure 2 Transfer Characteristics

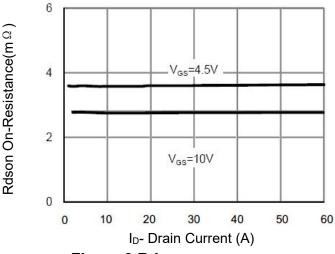
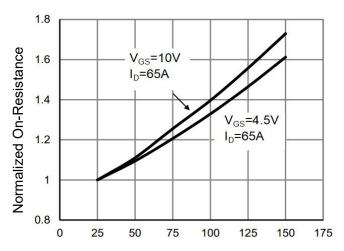


Figure 3 Rdson- Drain Current



T_J-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature

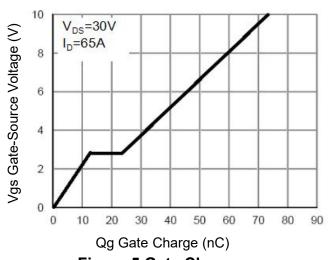


Figure 5 Gate Charge

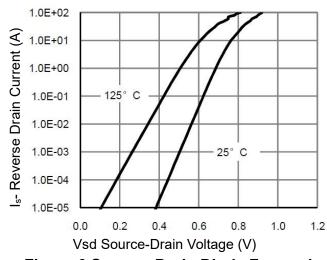


Figure 6 Source- Drain Diode Forward

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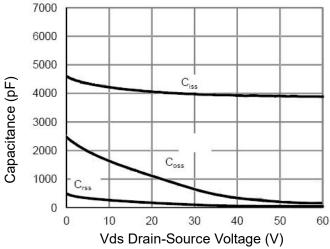


Figure 7 Capacitance vs Vds

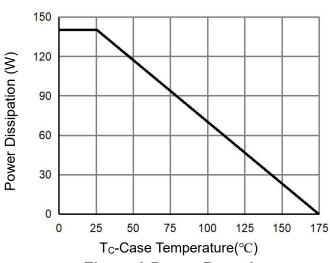


Figure 9 Power De-rating

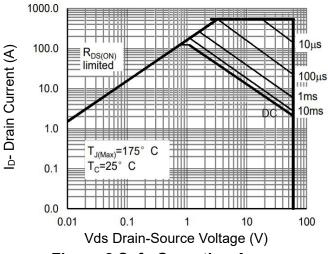


Figure 8 Safe Operation Area (Note3)

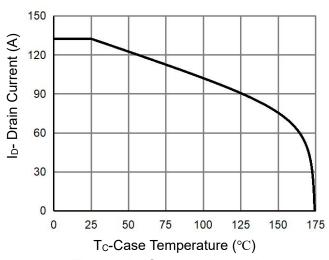
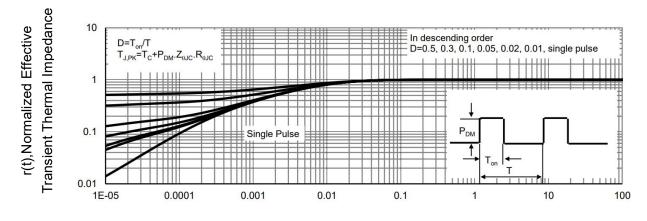


Figure 10 Current De-rating



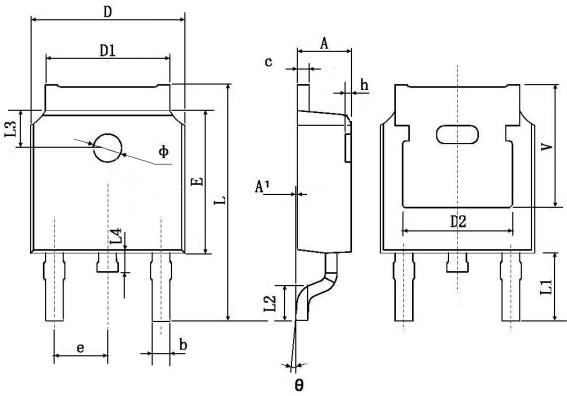
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

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TO-252 Package Information



O	Dimensions In Millimeters		Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.48	3 TYP.	0.190	TYP.		
Е	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.90	0 TYP.	0.114	0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600 TYP.		0.063	TYP.		
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.35	0 TYP.	0.211	0.211 TYP.		

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