

QIAOXIN N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

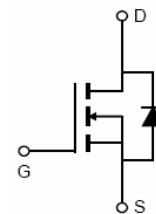
General Features

- $V_{DS} = 120V, I_D = 60A$
- $R_{DS(on)} = 12m\Omega$, typical@ $V_{GS} = 10V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

TO-252



Top View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package
VCRR12N12K		TO-252-2L

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	60	A
Drain Current-Continuous($T_c=100^\circ C$)	$I_D (100^\circ C)$	42	A
Pulsed Drain Current	I_{DM}	240	A
Maximum Power Dissipation	P_D	90	W
Derating factor		0.6	W/°C
Single pulse avalanche energy ^(Note 4)	E_{AS}	352	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.67	°C/W
--------------------------------------	-----------------	------	------

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=250\mu\text{A}$	120		-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=120\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{D}}=250\mu\text{A}$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_{\text{D}}=30\text{A}$	-	12	13	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_{\text{D}}=30\text{A}$		60	-	S
Dynamic Characteristics (Note 3)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $F=1.0\text{MHz}$	-	2230	-	pF
Output Capacitance	C_{oss}		-	170	-	pF
Reverse Transfer Capacitance	C_{rss}		-	19	-	pF
Switching Characteristics (Note 3)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=60\text{V}, \text{I}_{\text{D}}=30\text{A}$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_G=1.6\Omega$	-	13	-	nS
Turn-on Rise Time	t_r		-	10	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	8	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=60\text{V}, \text{I}_{\text{D}}=30\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	-	45	-	nC
Gate-Source Charge	Q_{gs}		-	15	-	nC
Gate-Drain Charge	Q_{gd}		-	14.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 2)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{S}}=30\text{A}$	-	-	1.2	V
Diode Forward Current	I_{S}		-	-	60	A
Reverse Recovery Time	t_{rr}	$\text{T}_J = 25^\circ\text{C}, \text{I}_{\text{F}} = 30\text{A}$ $d\text{i}/dt = 100\text{A}/\mu\text{s}$ (Note 3)	-	60	-	nS
Reverse Recovery Charge	Q_{rr}		-	106	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
3. Guaranteed by design, not subject to production
4. EAS condition : $\text{T}_J=25^\circ\text{C}, \text{V}_{\text{DD}}=50\text{V}, \text{V}_{\text{G}}=10\text{V}, \text{L}=0.25\text{mH}, \text{R}_G=25\Omega$

Typical Electrical and Thermal Characteristics

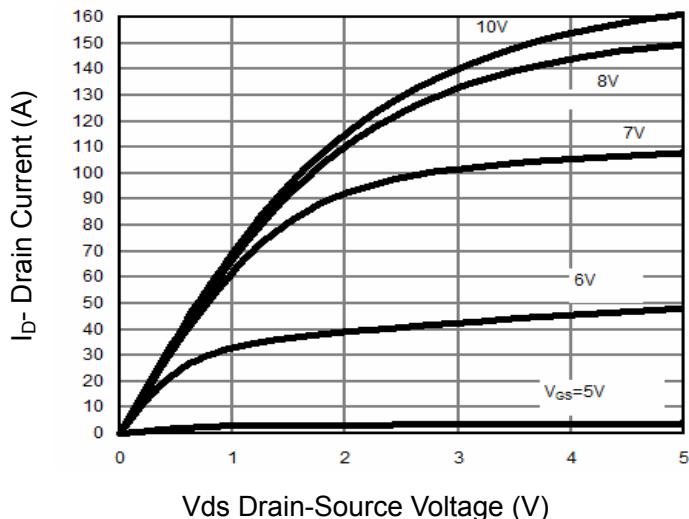


Figure 1 Output Characteristics

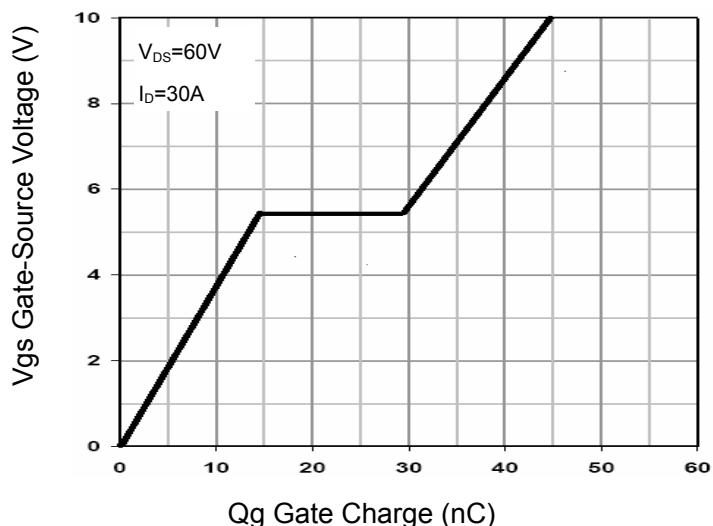


Figure 4 Gate Charge

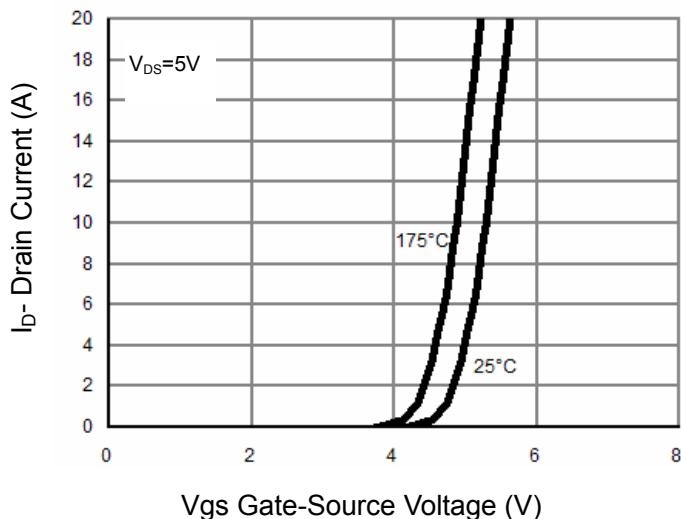


Figure 2 Transfer Characteristics

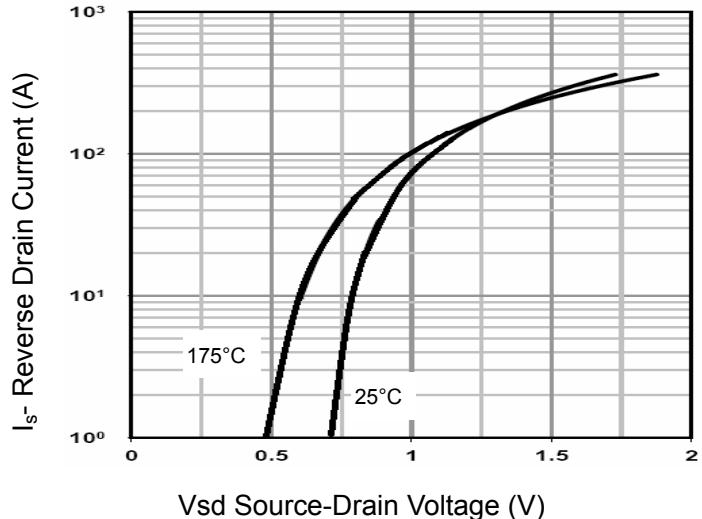


Figure 5 Source- Drain Diode Forward

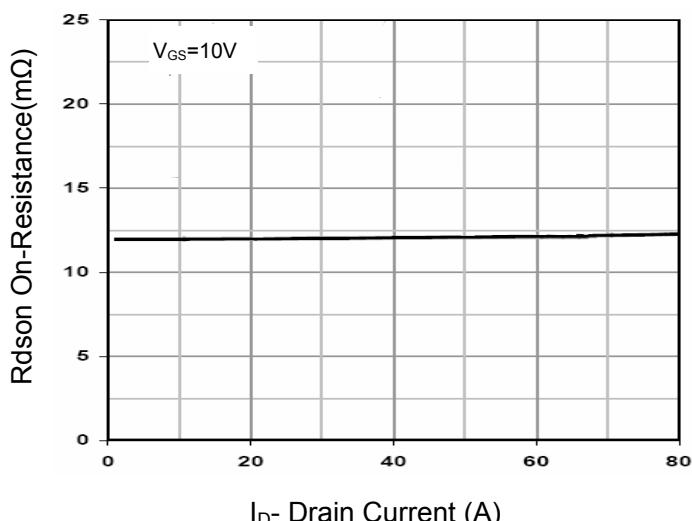


Figure 3 Rdson- Drain Current

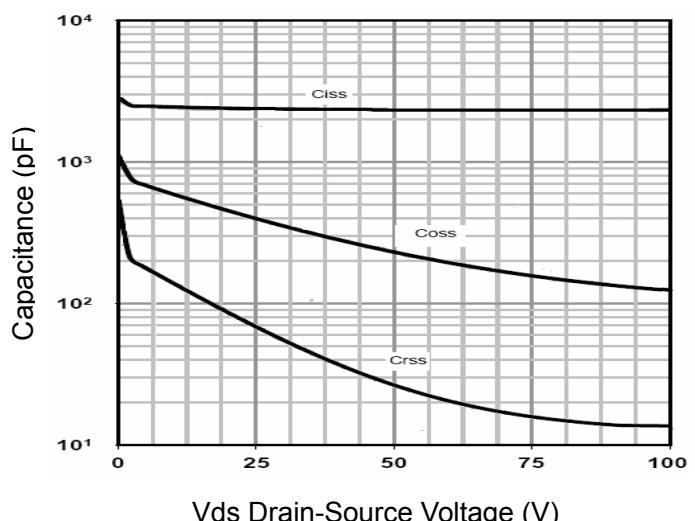


Figure 6 Capacitance vs Vds

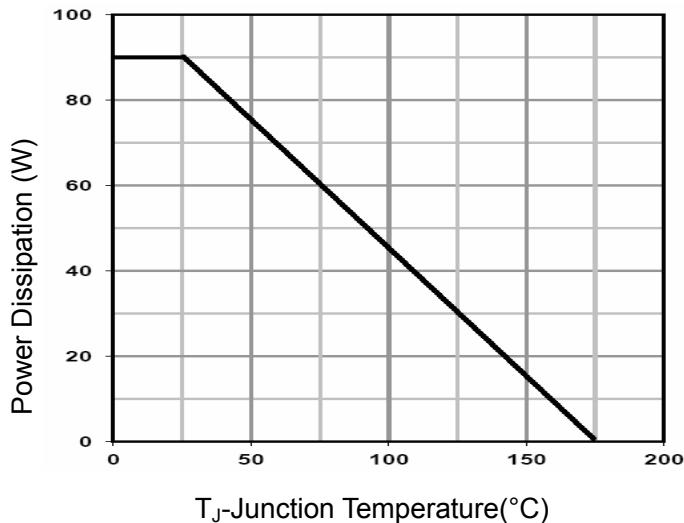


Figure 7 Power De-rating

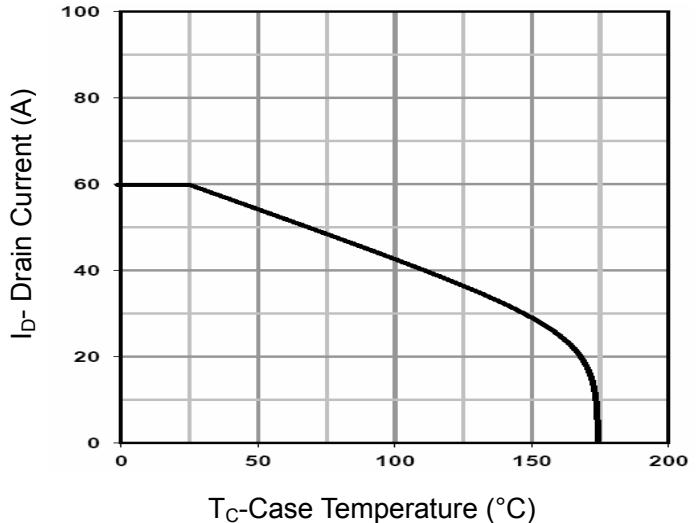


Figure 9 Current De-rating

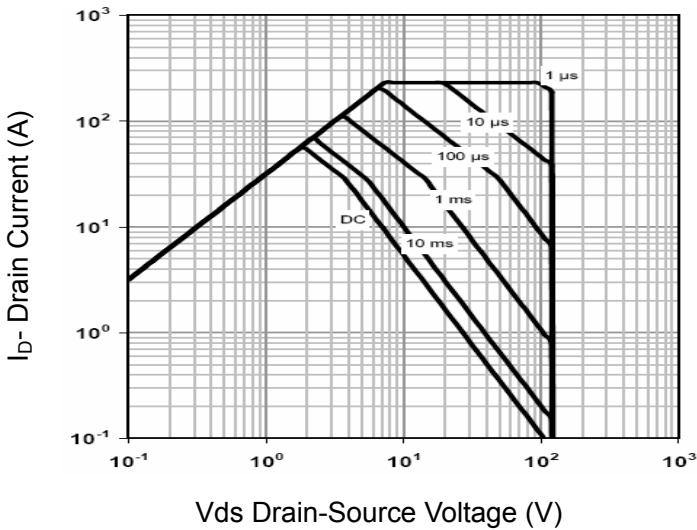


Figure 8 Safe Operation Area

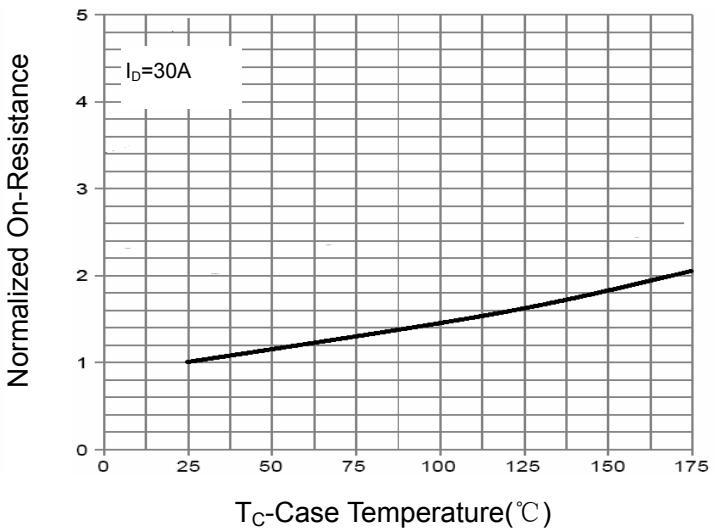


Figure 10 Rdson-Junction Temperature

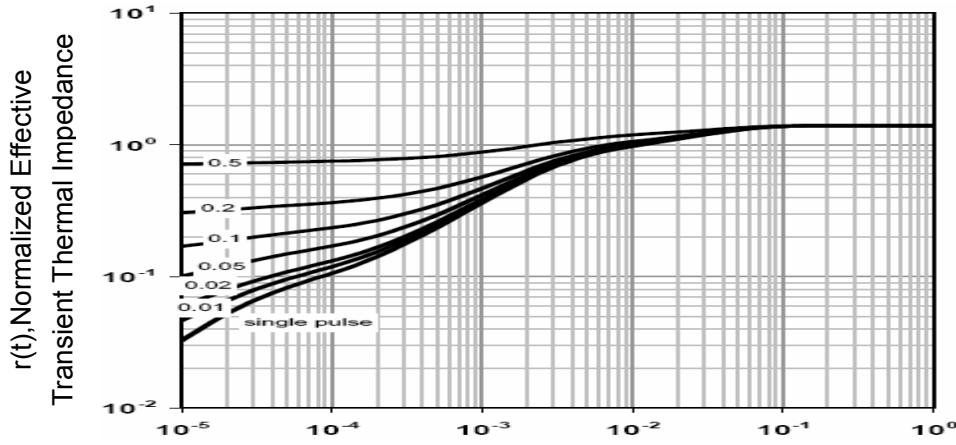
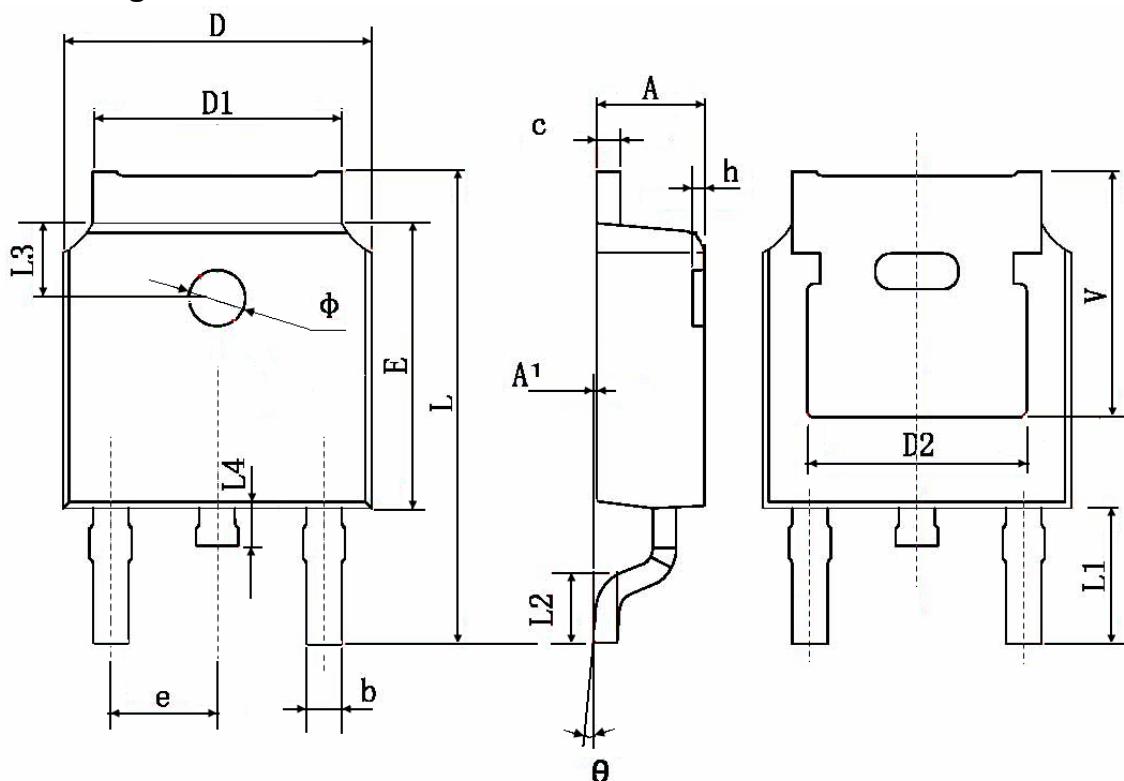


Figure 11 Transient Thermal Impedance

TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.83 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	

Attention

QIAOXIN assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all QIAOXIN products described or contained herein. QIAOXIN products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. QIAOXIN reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.