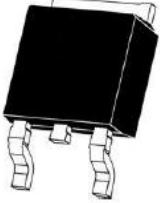
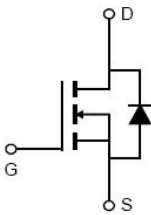


QIAOXIN N-Channel Super Trench II Power MOSFET

<p>Description</p> <p>The series of devices uses Super Trench II technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.</p> <p>Application</p> <ul style="list-style-type: none"> ● DC/DC Converter ● Ideal for high-frequency switching and synchronous rectification 	<p>General Features</p> <ul style="list-style-type: none"> ● $V_{DS} = 120V, I_D = 65A$ $R_{DS(ON)} = 9m\Omega$, typical @ $V_{GS} = 10V$ ● Excellent gate charge x $R_{DS(on)}$ product(FOM) ● Very low on-resistance $R_{DS(on)}$ ● 175 °C operating temperature ● Pb-free lead plating
<p>TO-252</p> 	 <p>Schematic Diagram</p>

Package Marking and Ordering Information

Device Marking	Device	Device Package
VCRR10N12AK		TO-252-2L

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	65	A
Drain Current-Continuous($T_C = 100^\circ C$)	$I_D(100^\circ C)$	46	A
Pulsed Drain Current	I_{DM}	260	A
Maximum Power Dissipation	P_D	120	W
Derating factor		0.8	W/ $^\circ C$
8Single pulse avalanche energy (Note 4)	E_{AS}	320	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.25	$^\circ C/W$
--------------------------------------	-----------------	------	--------------

Electrical Characteristics (T_c=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	120		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =120V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.7	2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =35A	-	9	10	mΩ
		V _{GS} =4.5V, I _D =35A	-	10.8	12	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =35A		60	-	S
Dynamic Characteristics (Note 3)						
Input Capacitance	C _{iss}	V _{DS} =60V, V _{GS} =0V, F=1.0MHz	-	4000	-	pF
Output Capacitance	C _{oss}		-	234	-	pF
Reverse Transfer Capacitance	C _{rss}		-	20	-	pF
Switching Characteristics (Note 3)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =60V, I _D =35A V _{GS} =10V, R _G =1.6Ω	-	16	-	nS
Turn-on Rise Time	t _r		-	15	-	nS
Turn-Off Delay Time	t _{d(off)}		-	30	-	nS
Turn-Off Fall Time	t _f		-	8	-	nS
Total Gate Charge	Q _g	V _{DS} =60V, I _D =35A, V _{GS} =10V	-	70.8	-	nC
Gate-Source Charge	Q _{gs}		-	12.9	-	nC
Gate-Drain Charge	Q _{gd}		-	18.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 2)	V _{SD}	V _{GS} =0V, I _S =35A	-	-	1.2	V
Diode Forward Current	I _S		-	-	65	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S di/dt = 100A/μs (Note 3)	-	80	-	nS
Reverse Recovery Charge	Q _{rr}		-	185	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
3. Guaranteed by design, not subject to production
4. EAS condition : T_j=25°C, V_{DD}=40V, V_G=10V, L=0.25mH, R_G=25Ω
5. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_J(MAX)=175° C. The SOA curve provides a single pulse rating

Typical Electrical and Thermal Characteristics

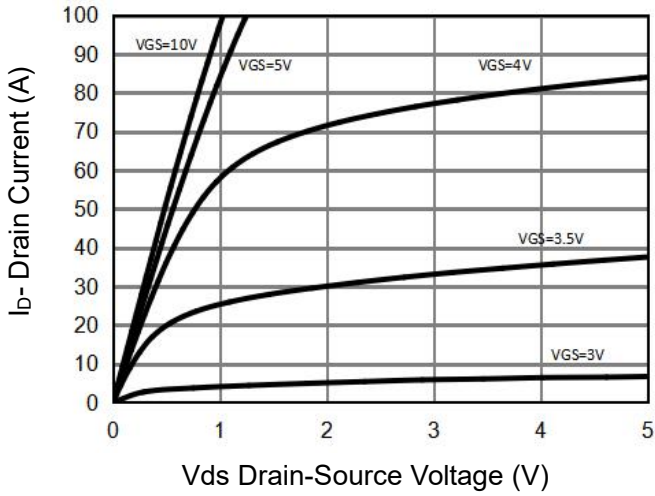


Figure 1 Output Characteristics

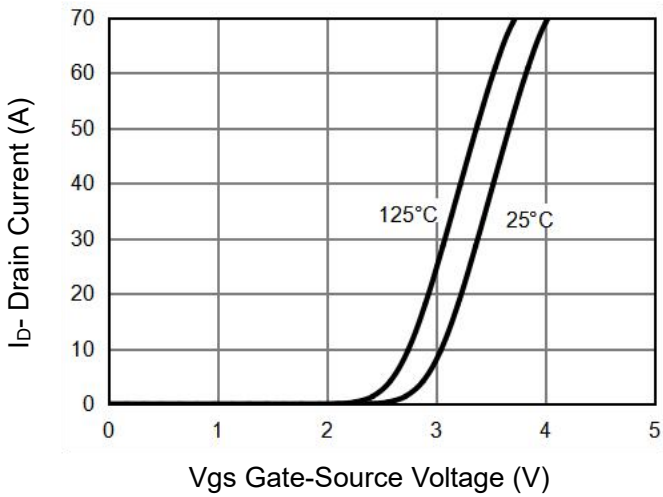


Figure 2 Transfer Characteristics

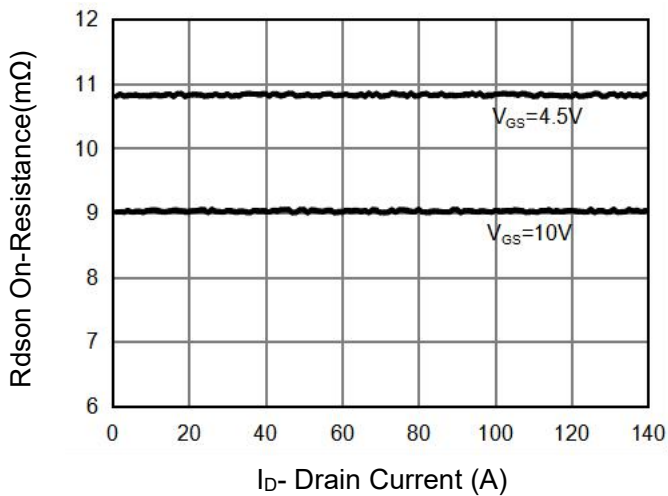


Figure 3 Rdson- Drain Current

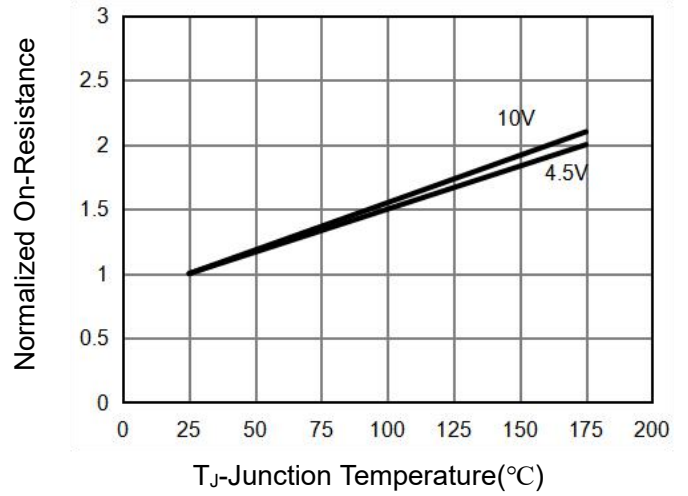


Figure 4 Rdson-Junction Temperature

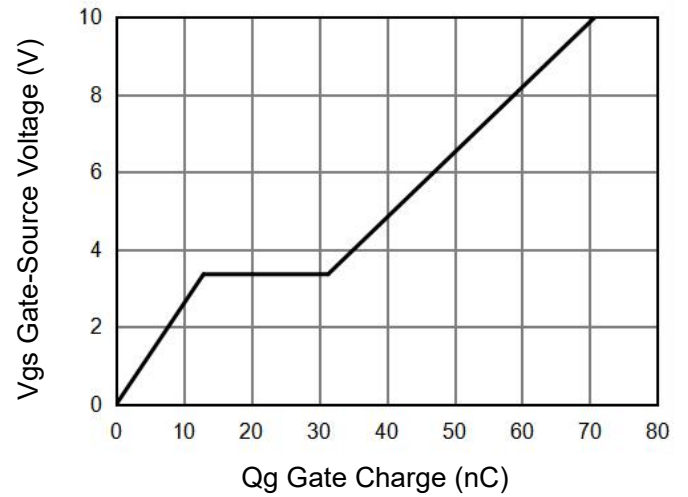


Figure 5 Gate Charge

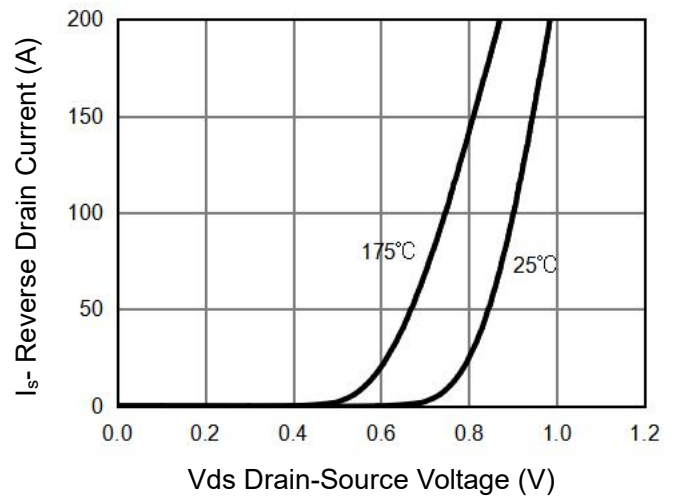


Figure 6 Capacitance vs Vds

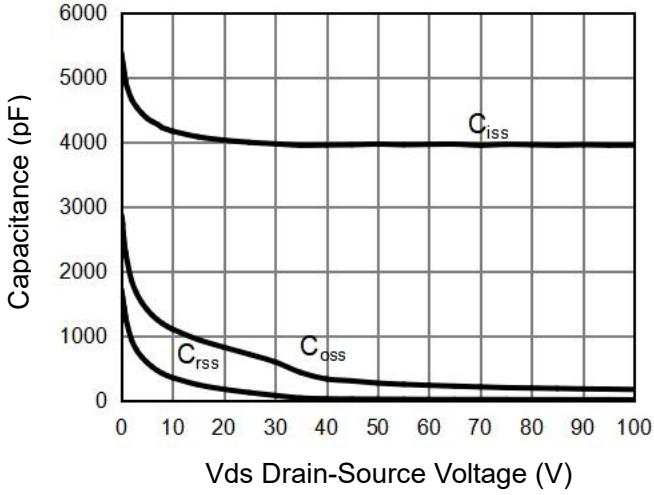


Figure 7 Capacitance vs Vds

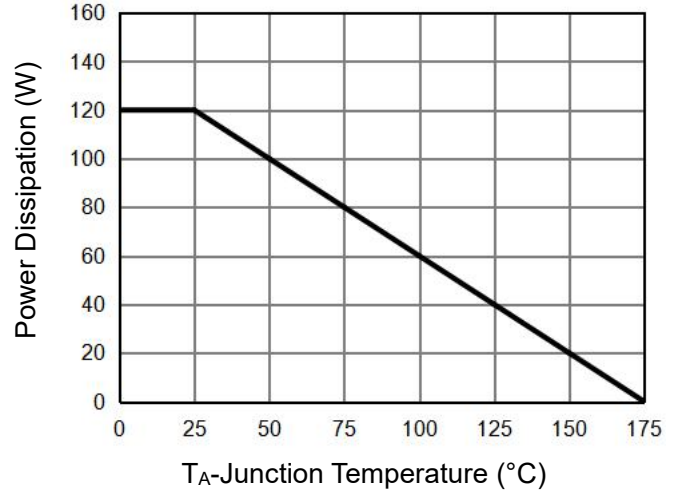


Figure 9 Power De-rating

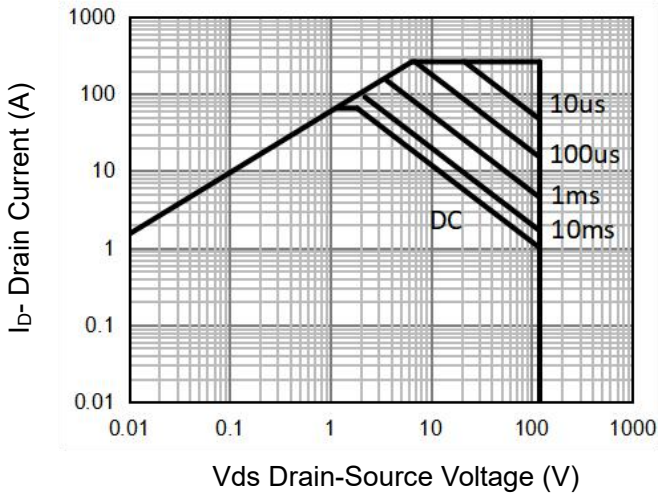


Figure 8 Safe Operation Area (Note 5)

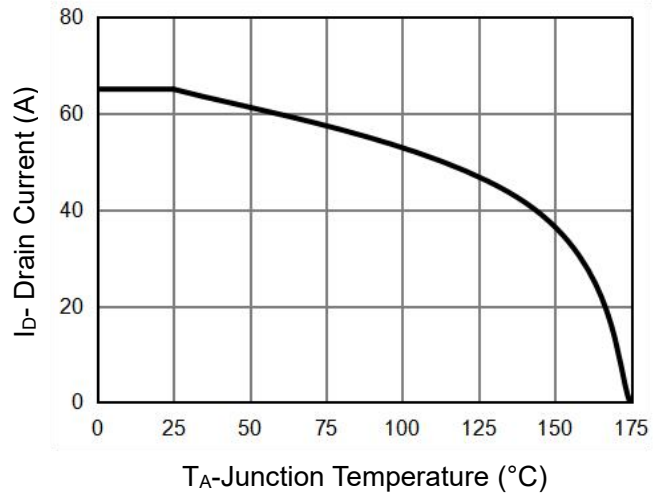


Figure 10 Current De-rating

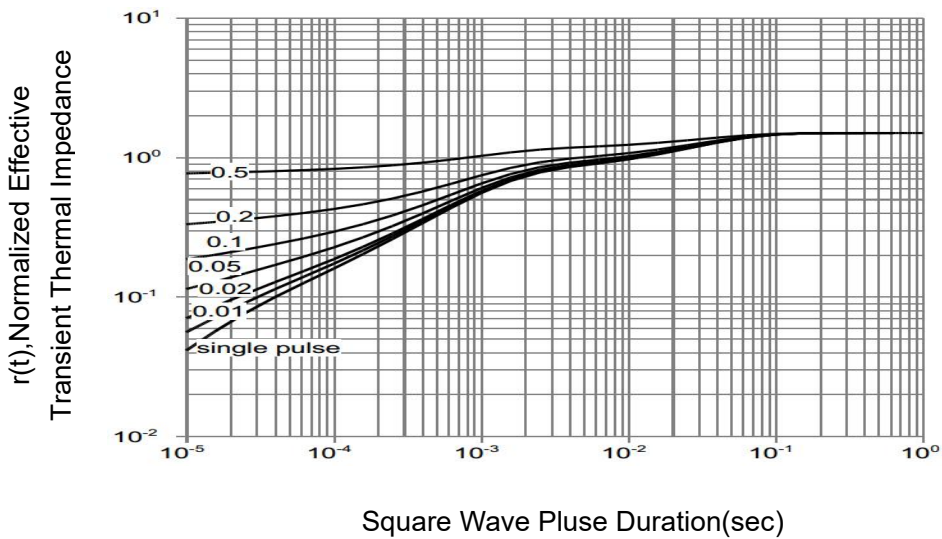
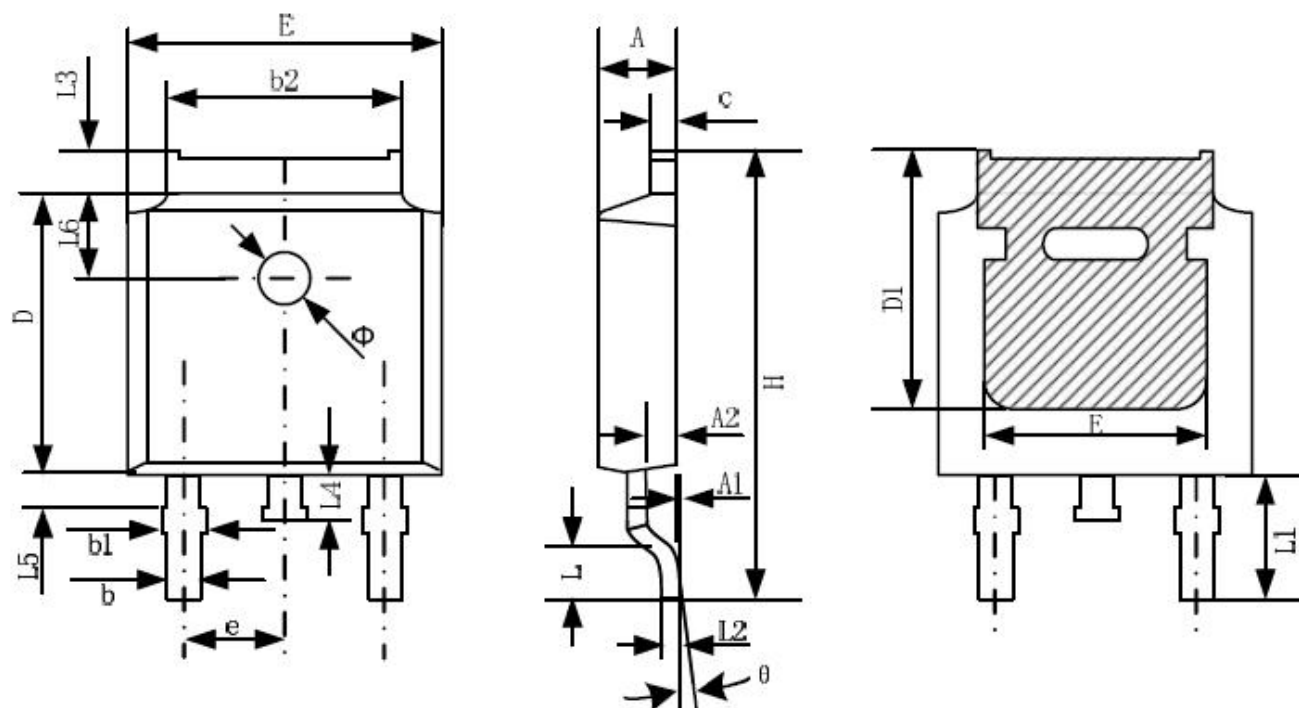


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.38	0.087	0.094
A1	0.00	0.10	0.000	0.004
A2	0.90	1.10	0.035	0.043
b	0.72	0.85	0.028	0.033
b1	0.72	0.90	0.028	0.035
b2	5.13	5.46	0.202	0.215
c	0.47	0.60	0.019	0.024
D	6.00	6.20	0.236	0.244
D1	5.25	--	0.207	--
E	6.50	6.70	0.256	0.264
E1	4.70	--	0.185	--
e	2.19	2.39	0.086	0.094
H	9.80	10.40	0.386	0.409
L	1.40	1.70	0.055	0.067
L1	2.90 REF		0.114 REF	
L2	0.508 BSC		0.020 BSC	
L3	0.90	1.25	0.035	0.049
L4	0.60	1.00	0.024	0.039
L5	0.15	0.75	0.006	0.030
L6	1.80 REF		0.071 REF	
Φ	1.20	1.40	0.047	0.055
θ	0°	8°	0°	8°

Attention

QIAOXIN assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all QIAOXIN products described or contained herein. QIAOXIN products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. QIAOXIN reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.