

1200V, 50A, Trench FS II Fast IGBT

General Description:

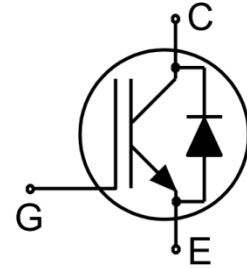
Using VCRR's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1200V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

Features

- Trench FSII Technology Offering
- Very low $V_{CE(sat)}$
- High speed switching
- Positive temperature coefficient in $V_{CE(sat)}$
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

Application

- Inverters
- Motor drives
- Converter



Schematic diagram

Package Marking and Ordering Information

Device	Device Package	Device Marking
VCRR50TD120BT	TO-247	VCRR50TD120BT



TO-247

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate- Emitter Voltage	± 30	V
I_C	Collector Current	100	A
	Collector Current @ $T_C = 100^\circ\text{C}$	50	A
I_{Cplus}	Pulsed Collector Current, t_p limited by T_{jmax}	150	A
-	turn off safe operating area, $V_{CE}=1200\text{V}$, $T_j=150^\circ\text{C}$	150	A
I_F	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	50	A
I_{FM}	Diode Maximum Forward Current	150	A
P_D	Power Dissipation @ $T_C = 25^\circ\text{C}$	535	W
	Power Dissipation @ $T_C = 100^\circ\text{C}$	268	W
T_j, T_{stg}	Operating Junction and Storage Temperature Range	-55 to +175	$^\circ\text{C}$
T_L	Maximum Temperature for Soldering	260	$^\circ\text{C}$
t_{sc}	Short circuit withstand time $V_{GE}=15.0\text{V}$, $V_{CC} \leq 600\text{V}$, Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$, $T_j \leq 150^\circ\text{C}$	10	μs

Thermal Characteristic

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Junction to case for IGBT	0.28	°C/W
R _{θJC}	Thermal Resistance, Junction to case for Diode	0.5	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics (T_C=25°C unless otherwise noted)

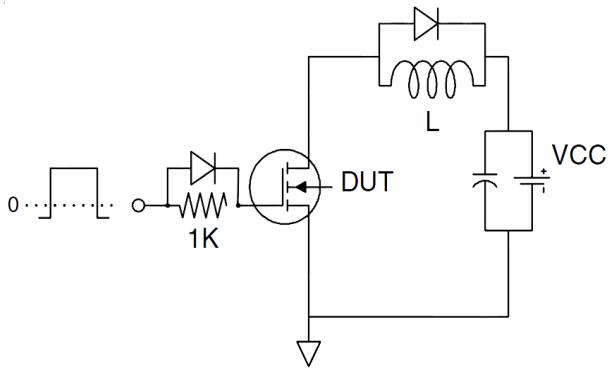
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
Static Characteristics						
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	V _{GE} =0V, I _{CE} =1mA	1200	--	--	V
I _{CES}	Collector-Emitter Leakage Current	V _{GE} =0V, V _{CE} =1200V	--	--	5	μA
I _{GES(F)}	Gate to Emitter Forward Leakage	V _{GE} =+30V, V _{CE} =0V	--	--	200	nA
I _{GES(R)}	Gate to Source Reverse Leakage	V _{GE} =-30V, V _{CE} =0V	--	--	200	nA
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =50A, T _J =25°C	--	1.55	1.8	V
		V _{GE} =15V, T _J =150°C	--	1.8	--	V
V _{GE(th)}	Gate Threshold Voltage	I _C =1mA, V _{CE} =V _{GE}	5.0	--	6.5	V
I _{C(SC)}	Short circuit collector current Max.1000 short circuits Time between short circuits: ≥1.0s	V _{GE} =15V, V _{CC} ≤600V, t _{SC} ≤10us, T _J ≤150°C	--	300	--	A
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} =30V, V _{GE} =0V, f=1MHz	--	6500	--	pF
C _{oes}	Output Capacitance		--	218	--	
C _{res}	Reverse Transfer Capacitance		--	180	--	
Q _g	Total Gate Charge	V _{CC} =960V, I _C =50A, V _{GE} =15V	--	381	--	nC
Q _{ge}	Gate to Emitter Charge		--	46	--	
Q _{gc}	Gate to Collector Charge		--	195	--	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{CE} =600V, I _C =50A, V _{GE} =0/15V, R _g =8Ω Inductive Load	--	19	--	ns
t _r	Rise Time		--	17	--	
t _{d(OFF)}	Turn-Off Delay Time		--	170	--	
t _f	Fall Time		--	18	--	
E _{on}	Turn-On Switching Loss		--	2.8	--	mJ
E _{off}	Turn-Off Switching Loss		--	2.0	--	
E _{ts}	Total Switching Loss		--	4.8	--	

Electrical Characteristics of the Diode(T_C= 25°C unless otherwise specified):

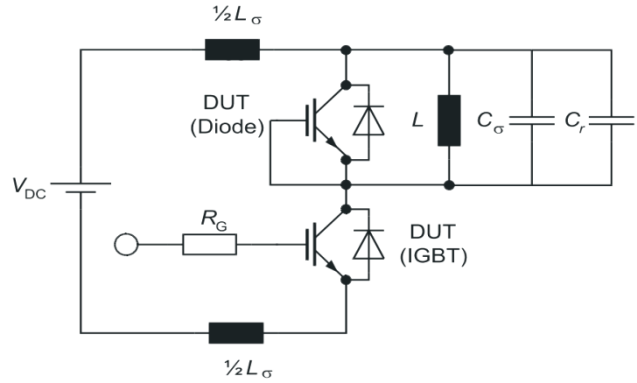
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V _{FM}	Diode Forward Voltage	I _F =50A	--	2.2	2.8	V
T _{rr}	Reverse Recovery Time	I _F =25A, di/dt=700A/us	--	150	--	ns
I _{RRM}	Diode Peak Reverse Recovery Current		--	10	--	A
Q _{rr}	Reverse Recovery Charge		--	2.2	--	μC
Pulse width t _{tp} ≤380μs, δ≤2%						

Test Circuit

1) Gate Charge Test Circuit

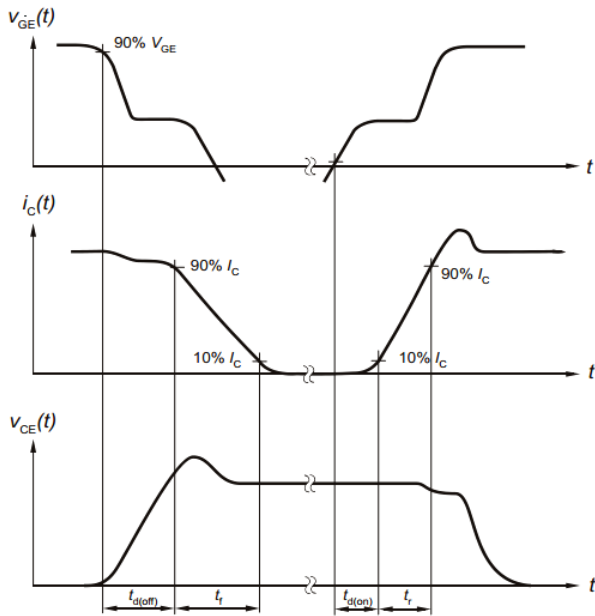


2) Switch Time Test Circuit

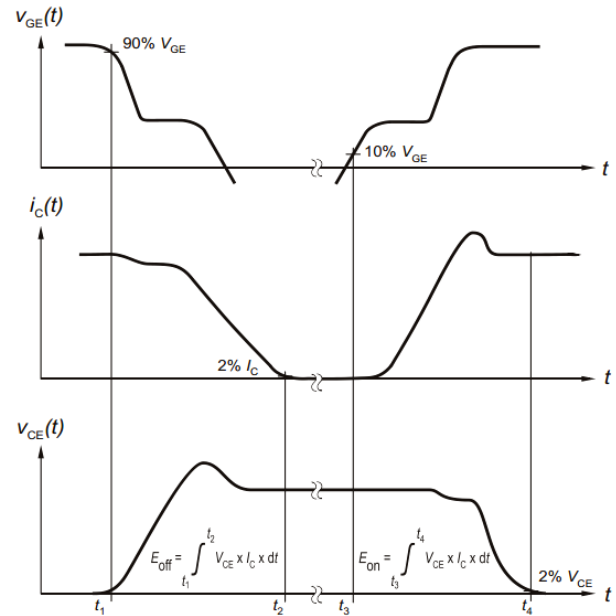


Switching characteristics

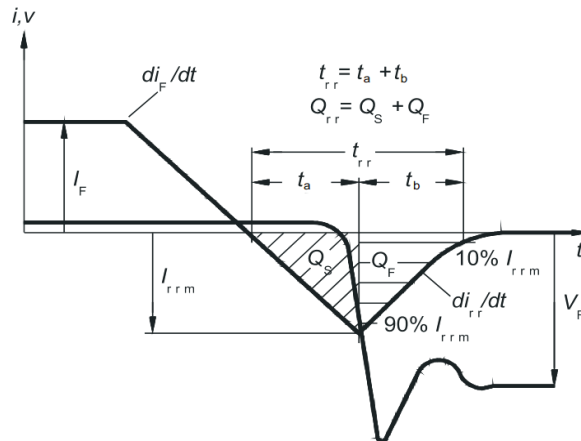
1) Definition of switching times



2) Definition of switching losses



3) Definition of diode switching characteristics



Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

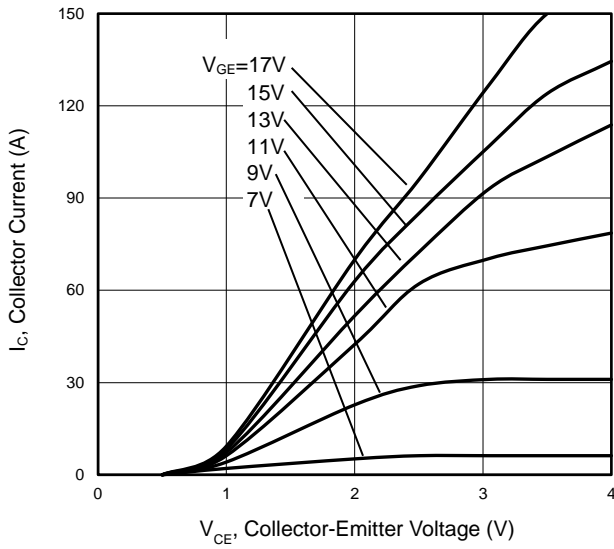


Figure 2 Transfer Characteristics

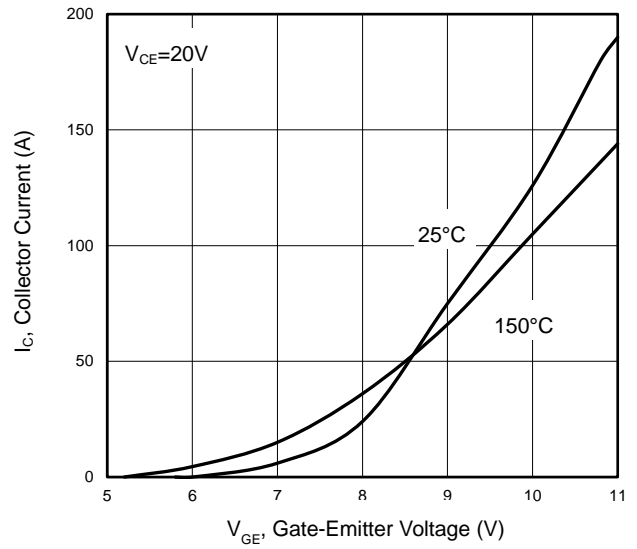


Figure 3 $V_{CE(sat)}$ vs. Case Temperature

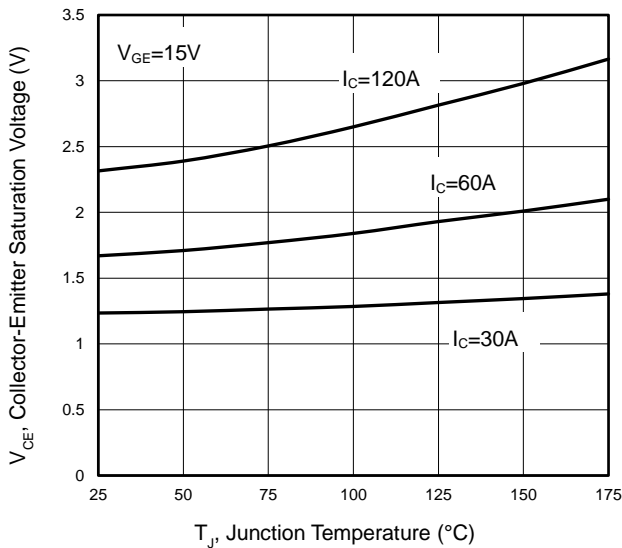


Figure 4 Saturation Voltage vs. V_{GE}

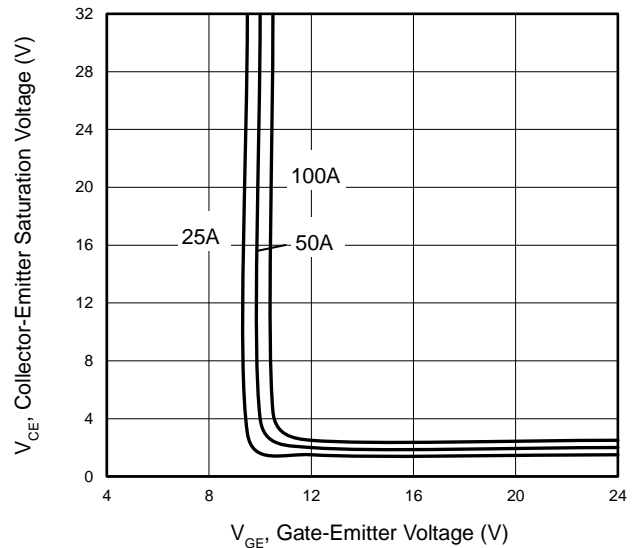


Figure 5 Capacitance Characteristics

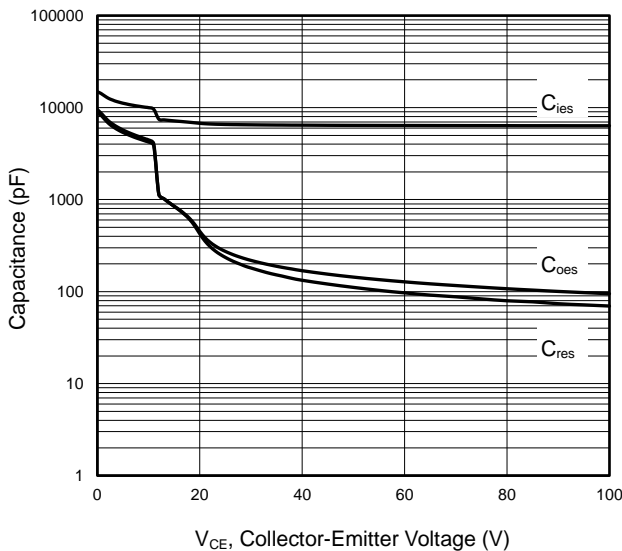
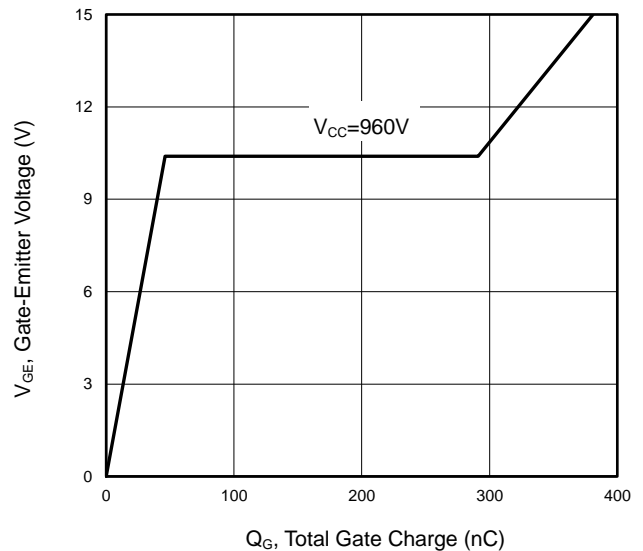
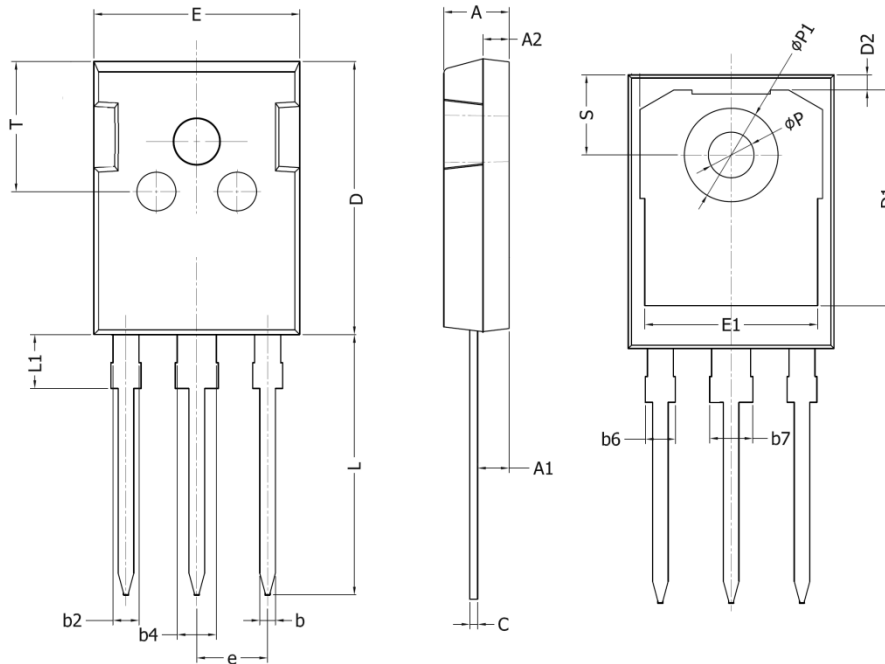


Figure 6 Gate Charge Wave Form



TO-247 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.90	5.10	0.193	0.201
A1	2.31	2.51	0.091	0.099
A2	1.9	2.1	0.075	0.083
b	1.16	1.26	0.046	0.050
b2	1.96	2.06	0.077	0.081
b4	2.96	3.06	0.117	0.120
b6	-	2.25	-	0.089
b7	-	3.25	-	0.128
C	0.59	0.66	0.023	0.026
D	20.90	21.10	0.823	0.831
D1	16.25	16.85	0.640	0.663
D2	1.05	1.35	0.041	0.053
E	15.70	15.90	0.618	0.626
E1	13.10	13.50	0.516	0.531
e	5.436 BSC		0.214 BSC	
L	19.80	20.10	0.780	0.791
L1	-	4.30	-	0.169
P	3.40	3.60	0.134	0.142
P1	7.00	7.40	0.276	0.291
S	6.05	6.25	0.238	0.246
T	9.80	10.20	0.386	0.402

Attention

QIAOXIN assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all QIAOXIN products described or contained herein. QIAOXIN products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. QIAOXIN reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.